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CUSTOMER	ACCEPTANCE SPEC	CIFICATIONS
FOR	DEL NO.: ETML121025LUDY (RoHS) MESSRS:	A Stiffaille
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BY:		

https://www.edtc.com/

https://smartembeddeddisplay.com/

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1. GENERAL SPECIFICATIONS

1.1 APPLICATION NOTES FOR CAPACITIVE TOUCH PANEL CONTROLLER/DRIVER PLEASE REFER TO:

TOUCHNETIX AX112A

1.2 MATERIAL SAFETY DESCRIPTION
ASSEMBLIES SHALL COMPLY WITH EUROPEAN ROHS REQUIREMENTS,
INCLUDING PROHIBITED MATERIALS/COMPONENTS CONTAINING LEAD,
MERCURY, CADMIUM, HEXAVALENT CHROMIUM, POLYBROMINATED
BIPHENYLS (PBB) AND POLYBROMINATED
DIPHENYL ETHERS (PBDE), BIS(2-ETHYLHEXYL) PHTHALATE (DEHP), BUTYL
BENZYL PHTHALATE (BBP), DIBUTYL PHTHALATE (DBP), DIISOBUTYL
PHTHALATE (DIBP).

2. MECHANICAL SPECIFICATIONS

2.1 LCD MODULE MECHANICAL SPECIFICATIONS

(1) DISPLAY SIZE	12.1 inch
(2) NUMBER OF DOTS	1280(BGR)W * 800H DOTS
(3) MODULE SIZE	283W * 185.1H *18.8D(MAX.) mm
	(WITHOUT FPC)
(4) VIEWING AREA	263.52W * 165.6H mm
(5) ACTIVE AREA	261.12W * 163.2H mm
(6) DOT SIZE	0.068W * 0.204H mm
(7) PIXEL SIZE	0.204W * 0.204H mm
(8) LCD TYPE	TFT , IPS , TRANSMISSIVE ,
	NORMALLY BLACK
(9) COLOR	16.7M
(10) VIEWING DIRECTION	SUPER WIDE VIEW
(11) BACK LIGHT	LED , COLOR : WHITE
(12) INTERFACE MODE	LVDS (8 BIT)
(13) WEIGHT	TBD

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2.2 CAPACITIVE TOUCH PANEL MECHANICAL SPECIFICATIONS (1) TOUCH PANEL SIZE	RGING DISPLAY	MODEL NO.	VERSION	PAGE
(1) TOUCH PANEL SIZE	HNOLOGIES CORPORATION	ETML121025LUDYA	1	2
(1) TOUCH PANEL SIZE	CAPACITIVE TOUCH PANEL M	IFCHANICAL SPECIFICATION:	3	
(2) OUTER DIMENSION	CHITICITY E TO CONTINUE IN		,	
(WITHOUT FPC) (3) ACTIVE AREA				
(3) ACTIVE AREA	(2) OUTER DIMENSION			
(4) INPUT TYPE	(3) ACTIVE AREA	· · · · · · · · · · · · · · · · · · ·		
(6) RESOLUTION 65536*65536 (7) INTERFACE MODE USB 2.0	· · ·			
(7) INTERFACE MODEUSB 2.0	(5) NUMBER OF TOUCH SENSOR	48*30 SENSORS		
(7) INTERFACE MODE	(6) RESOLUTION	65536*65536		
Complete the property of the control	(7) INTERFACE MODE	USB 2.0	77.	
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3. ABSOLUTE MAXIMUM RATINGS

3.1 LCD MODULE ELECTRICAL ABSOLUTE MAXIMUM RATINGS

ITEM	SYMBOL	MIN.	MAX.	UNIT	REMARK
POWER SUPPLY VOLTAGE	VDD-VSS	-0.3	5.0	V	_
POWER SUPPLY VOLTAGE FOR LED DRIVER	VBL+-VBL-	-0.3	27	V	_

NOTE (1): LCM SHOULD BE GROUND DURING LCM HANDLING.

3.2 CAPACITIVE TOUCH PANEL ELECTRICAL ABSOLUTE MAXIMUM RATINGS

ITEM	SYMBOL	MIN.	MAX.	UNIT REMARK
POWER SUPPLY VOLTAGE	VDD1-VSS1	1.25	6	V

3.3 ENVIRONMENTAL ABSOLUTE MAXIMUM RATINGS

ITEM	OPERATING		STORAGE		REMARK	
TTEM	MIN.	MAX.	MIN.	MAX.	REMARK	
AMBIENT TEMPERATURE	-30°C	85°C	-40°C	85°C	NOTE (1),(2),(3),(4)	
HUMIDITY	NOTI	E(3)	NOT	E(3)	WITHOUT CONDENSATION	
VIBRATION	_ <	2.45 m/s ² (0.25 G)	000	11.76 m/s ² (1.2 G)	10~100 Hz XYZ DIRECTIONS 1 HR EACH	
SHOCK		29.4 m/s ² (3 G)		490 m/s ² (50 G)	10 ms XYZ DIRECTIONS 1 TIME EACH	
CORROSIVE GAS	NOT ACC	EPTABLE	NOT ACC	EPTABLE		

- NOTE (1): THE ABSOLUTE MAXIMUM RATINGS OF THIS PRODUCT SHOULD NOT BE EXCEEDED AT ANY TIME, IF THESE RATINGS ARE EXCEEDED, THE PRODUCT'S PERFORMANCE IS NOT GUARANTEED AND THE PRODUCT MAY EXPERIENCE PERMANENT DAMAGE.
- NOTE (2) : BACKGROUND COLOR CHANGES SLIGHTLY DEPENDING ON AMBIENT TEMPERATURE THIS PHENOMENON IS REVERSIBLE.
- NOTE (3): $Ta \le 60^{\circ}C : 90\%RH MAX. (96HRS MAX.)$
 - Ta > 60°C: ABSOLUTE HUMIDITY MUST BE LOWER THAN THE HUMIDITY OF 90%RH AT 60°C. (96HRS MAX.)
- NOTE (4): WHEN THE LCD MODULE IS OPERATED AT A HIGHER AMBIENT TEMPERATURE THAN 60°C, THE PWM DUTY CYCLE OF THE LED BACKLIGHT SHOULD BE ADJUSTED TO BE LESS THAN (TBD)%. IF THE MODULE IS OPERATED AT A HIGHER DUTY CYCLE THAN (TBD)%, THEN THERE IS A POSSIBILITY OF DISTORTION AND IRREGULARITY OF THE PICTURE DUE TO LIQUID CRYSTAL BEHAVIOR.

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4. ELECTRICAL CHARACTERISTICS

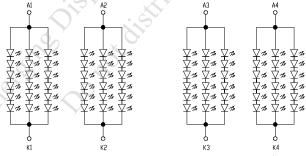
4.1 LCD MODULE ELECTRICAL CHARACTERISTICS

 $Ta = 25 \, ^{\circ}C$

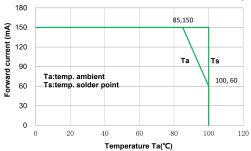
ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	REMARK
POWER SUPPLY VOLTAGE	VDD-VSS	_	(3.15)	(3.30)	(3.45)	V	
LOGIC HIGH INPUT VOLTAGE	VIH	_	0.8*VDD	_	VDD	V	
LOGIC LOW INPUT VOLTAGE	VIL	_	0		VDD*0.2	V	
PERMISSIVE POWER SUPPLY INPUT RIPPLE	VDDrp	VDD-VSS=3.30V		_	100	mV_{p-p}	
INRUSH CURRENT	IRUSH		_	_	2.0	Α	NOTE (6)
POWER SUPPLY CURRENT	IDD	VDD-VSS=3.30V	_	(150)	(250)	mA	NOTE (1)
POWER SUPPLY VOLTAGE FOR LED DRIVER	VBL+-VBL-		(10.8)	(12.0)	(13.2)	V	NOTE (2)
LOGIC HIGH INPUT VOLTAGE FOR BL_EN, BL_PWM	VIH	10 - KG	1.6	20		V	
LOGIC LOW INPUT VOLTAGE FOR BL_EN, BL_PWM	VIL		\$205 105		0.8	V	
POWER SUPPLY CURRENT FOR LED DRIVER	IBL	VBL+-VBL- =12.0V LED B/L=ON PWM=100%) 	(1.8)	(3.0)	A	
LED LIFE TIME		IF=68mA (PER LED)	50000	_		HRS	NOTE (4) NOTE (5)

NOTE (1): THE DISPLAY PATTERN IS ALL "WHITE".

NOTE (2): INTERNAL CIRCUIT DIAGRAM OF BACKLIGHT.



NOTE (3): AMBIENT TEMP. VS. ALLOWABLE FORWARD CURRENT. (PER LED)



NOTE (4): CONDITIONS; Ta=25 °C, CONTINUOUS LIGHTING

EMERGING DISPLAY

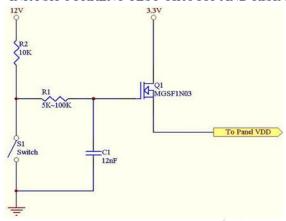
TECHNOLOGIES CORPORATION

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NOTE (5): DEFINITIONS OF LIFE TIME:

LCM LUMINANCE BECOMES HALF OF THE INITIAL VALUE.

NOTE (6): INRUSH CURRENT TEST CIRCUIT AND RISING TIME SETTING (POWER ON)

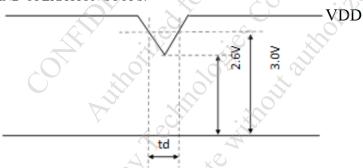


VSS 10% - VDD VDD VSS Rising time

Test circuit

VDD-DIP CONDITION:

WHEN VDD>2.6V OR VDD>3.0V AND 10ms>td, THIS PRODUCT SHOULD WORK NORMALLY. IT SHOULD BE RETURNED NORMALLY AFTER VDD RETURN TO MORE THAN 2.6V EVEN IF VDD DROPS 2.6V AND OPERATION STOPS.



4.2 CAPACITIVE TOUCH PANEL ELECTRICAL CHARACTERISTICS

Ta = 25 °C

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT
POWER SUPPLY FOR DRIVER	VDD1-VSS1	_	4.75	5.00	5.25	V
POWER SUPPLY CURRENT	IDD1	VDD1-VSS1 =5.00V	_	TBD	TBD	mA

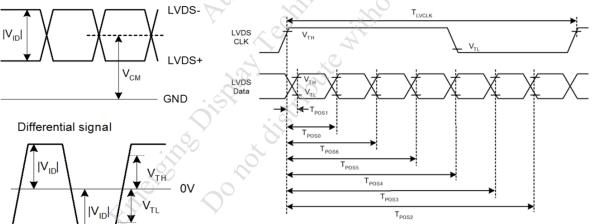
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5. TIMING CHARACTERISTICS

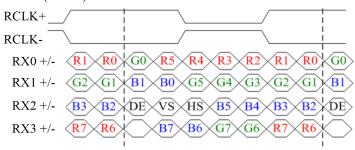
5.1 SWITCHING CHARACTERISTICS FOR LVDS RECEIVER

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT
DIFFERENTIAL INPUT HIGH THRESHOLD	V_{TH}	_	_	_	200	mV
DIFFERENTIAL INPUT LOW THRESHOLD	V_{TL}		-200	_		mV
DIFFERENTIAL INPUT COMMON MODE VOLTAGE	V _{CM}		1.0	1.2	1.7- VID / 2	V
INPUT LEAKAGE CURRENT	$I_{\rm IN}$	RX+/-, RXC+/-	-1.0		10	uA
DIFFERENTIAL INPUT VOLTAGE	V _{ID}	_	200		600	mV
1 DATA BIT TIME	UI	_	_	1/7	_	TLVCLK
POSITION 1	T _{POS1}		-0.25	0	0.25	UI
POSITION 0	T _{POS0}	_	0.75	1	1.25	UI
POSITION 6	T _{POS6}	_	1.75	2	2.25	UI
POSITION 5	T _{POS5}		2.75	3	3.25	UI
POSITION 4	T _{POS4}	× – (3.75	4	4.25	UI
POSITION 3	T _{POS3}		4.75	5	5.25	UI
POSITION 2	T _{POS2}	-62	5.75	6	6.25	UI





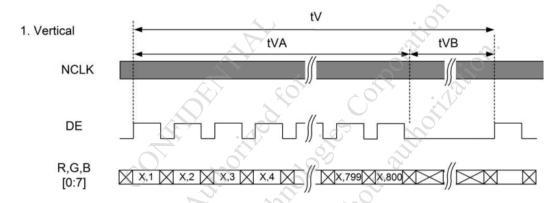
5.2 BIT LVDS INPUT 8-BIT LVDS FROMAT(VESA)

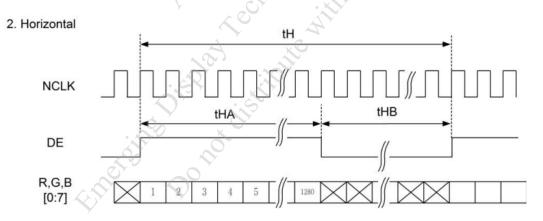


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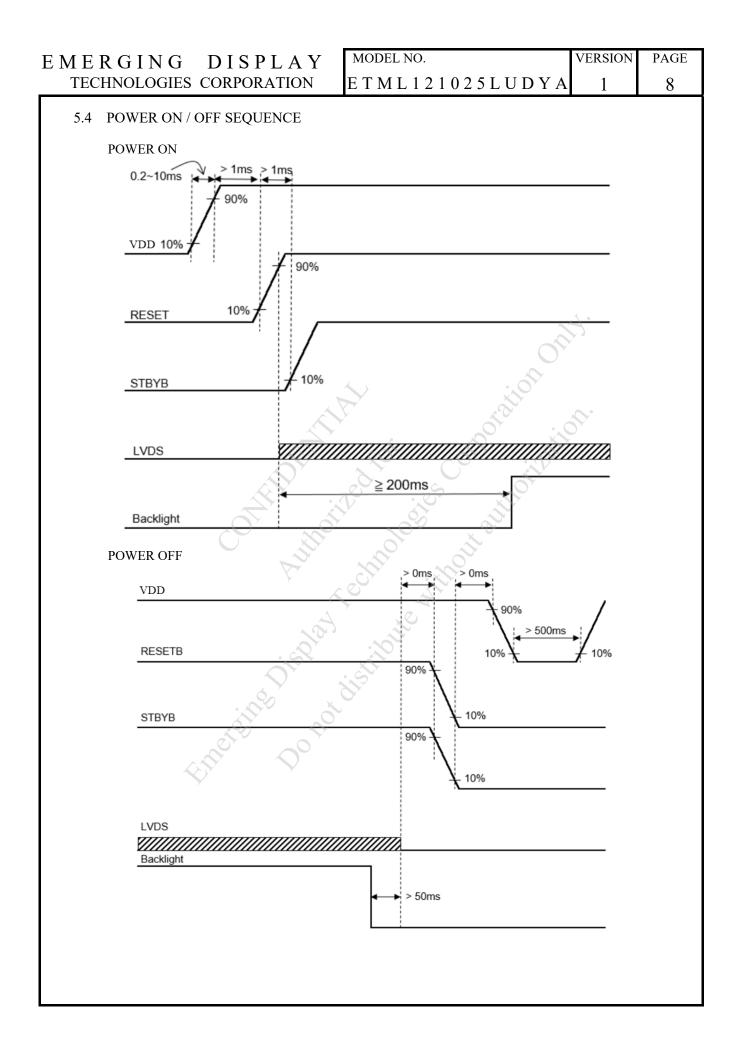
5.3 INTERFACE TIMING (DE MODE ONLY)

ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT
FRAME RATE	_		60		Hz
HORIZONTAL DISPLAY TIME	tHA		1280		clock
HORIZONTAL BLANKING TIME	tHB	70	80	90	clock
HORIZONTAL TOTAL TIME	tH	1350	1360	1370	clock
VERTICAL DISPLAY TIME	tVA		800		line
VERTICAL BLANKING TIME	tVB	15	30	45	line
VERTICAL TOTAL TIME	tV	815	830	845	line
CLOCK RATE	1/T _{CLOCK}	66.015	67.728	69.459	MHz





NOTE (1): ALL TIMING PARAMETERS SHOULD BE CONSTANT IN EACH FRAME.



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6. OPTICAL CHARACTERISTICS

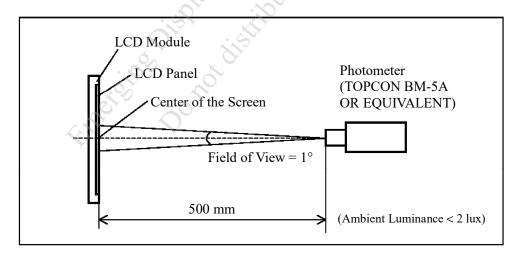
6.1 OPTICAL CHARACTERISTICS

 $Ta = 25 \pm 2$ °C

ITEM		SYMBOL	COND	ITION	MIN.	TYP.	MAX.	UNIT	REMARK	
		θ_{y^+}		$\theta_{x}=0^{\circ}$		88				
VIEWING ANGLE		$\theta_{ ext{y-}}$	CD > 10		80	88	_	J	NOTE (2)	
VIEWING ANGLE		θ_{x^+}	CR ≥ 10	0 -00	80	88		deg.	NOTE (3)	
		θ_{x}		$\theta_y=0^\circ$	80	88				
CONTRAST RATIO)	CR			(800)	(1000)			NOTE(3)	
(CENTER)		OK .	θx=0°,	θ y=0°	(000)	(1000)	<u> </u>	1.	NOTE (3)	
RESPONSE TIME		TR+TF			_	_	30	msec	NOTE (4)	
WHITE		Wx			(0.26)	(0.31)	(0.36)			
	WILLE	Wy	4		(0.30) (0.35)	(0.40)	_			
COLOR	RED	Rx		$\theta_{v}=0^{\circ}$ $\theta_{v}=0^{\circ}$	(0.60)	(0.65)	(0.70)			
COLOR CHROMATICITY	KED	Ry	0- 00		(0.30)	(0.35)	(0.40)	20.	NOTE (5)	
(CENTER)	GREEN	Gx			(0.28)	(0.33)	(0.38)		NOIE (3)	
	OKEEN	Gy	~	BL-=12V	(0.57)	(0.62)	(0.67)			
BLUE		Bx	LED B	J.	(0.09)	(0.14)	(0.19)			
		By	PWM=	=100%	(0.06)	(0.11)	(0.16)			
THE BRIGHTNESS OF MODULE (CEN		В	Oit	200	(1250)	(1350)	_	cd/m ²	NOTE (6)	
THE UNIFORMITY MODULE	OF		57	allo,	70		_	%	NOTE (7)	

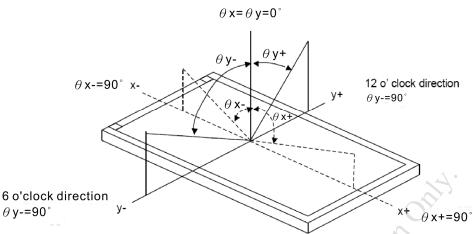
NOTE (1): TEST CONDITION:

AFTER STABILIZING AND LEAVING THE PANEL ALONE AT A GIVEN TEMPERATURE FOR 30 MINUTES. MEASUREMENT SHOULD BE EXECUTED IN A STABLE, WINDLESS, AND DARK ROOM.



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NOTE (2): DEFINITION OF VIEWING ANGLE:

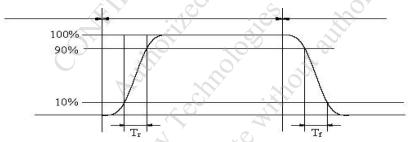


Normal

NOTE (3): DEFINITION OF CONTRAST RATIO (CR): MEASURED AT THE CENTER POINT OF MODULE

 $\label{eq:contrast_ratio} \text{CONTRAST} \quad \text{RATIO(CR)} = \frac{\text{BRIGHTNESS MEASURED WHEN LCD IS AT "WHITE STATE"}}{\text{BRIGHTNESS MEASURED WHEN LCD IS AT "BLACK STATE"}}$

NOTE (4): DEFINITION OF RESPONSE TIME: T_r AND T_f THE FIGURE BELOW IS THE OUTPUT SIGNAL OF THE PHOTO DETECTOR.



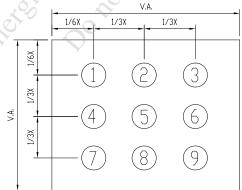
NOTE (5): DEFINITION OF COLOR CHROMATICITY

(a)100% RGB PIXEL DATA TRANSMISSION WHEN ALL THE INPUT TERMINALS OF MODULE ARE ELECTRICALLY POWERED ON.

(b)MEASURED AT THE CENTER POINT OF MODULE

NOTE (6): MEASURED THE BRIGHTNESS OF WHITE STATE AT CENTER POINT.

NOTE (7): (a) DEFINITION OF BRIGHTNESS UNIFORMITY

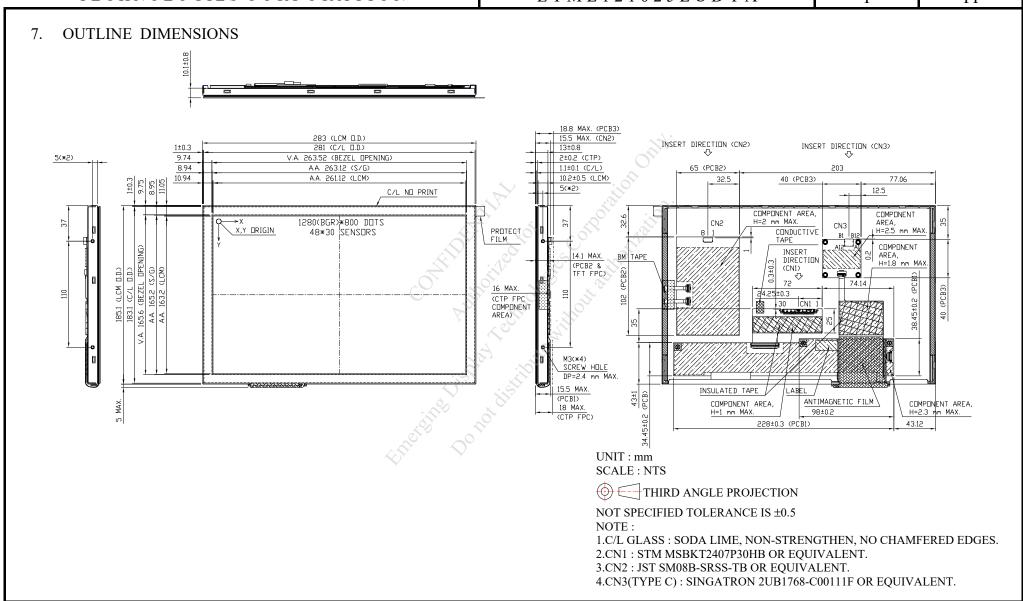


UNIT: mm

(b)THE BRIGHTNESS UNIFORMITY CALCULATING METHOD

UNIFORMITY: $\frac{\text{MINIMUM BRIGHTNESS}}{\text{MAXIMUM BRIGHTNESS}} *100\%$

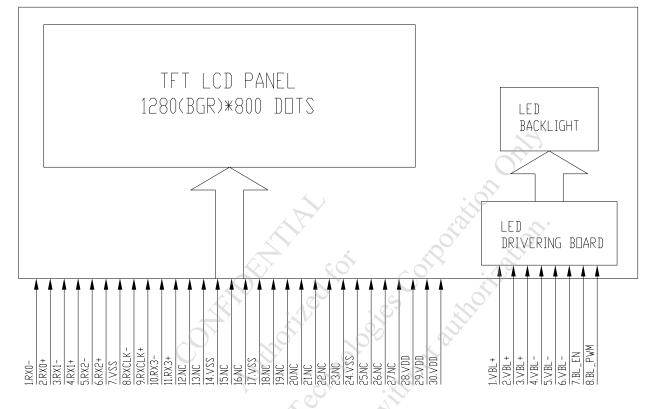
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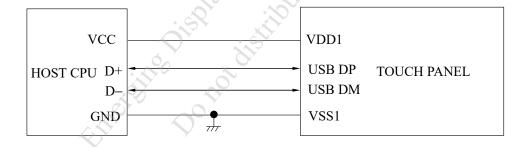
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8. BLOCK DIAGRAM

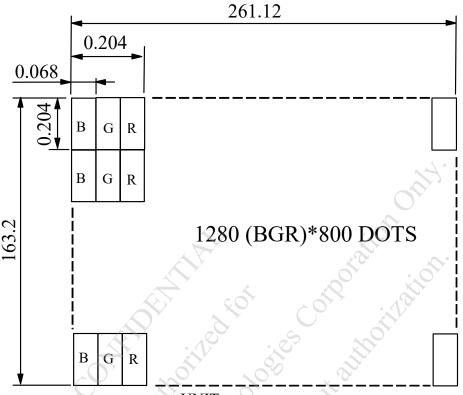
8.1 LCD MODULE



8.2 CTP MODULE







UNIT : mm SCALE : NTS

NOT SPECIFIED TOLERANCE IS ± 0.1 DOTS MATRIX TOLERANCE IS ± 0.01

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10. INTERFACE SIGNALS

10.1 CN1

PIN NO.	SYMBOL	FUNCTION
1	RX0-	NEGATIVE TRANSMISSION DATA OF PIXEL0
2	RX0+	POSITIVE TRANSMISSION DATA OF PIXEL0
3	RX1-	NEGATIVE TRANSMISSION DATA OF PIXEL1
4	RX1+	POSITIVE TRANSMISSION DATA OF PIXEL1
5	RX2-	NEGATIVE TRANSMISSION DATA OF PIXEL2
6	RX2+	POSITIVE TRANSMISSION DATA OF PIXEL2
7	VSS	GROUND
8	RXCLK-	NEGATIVE OF CLOCK
9	RXCLK+	POSITIVE OF CLOCK
10	RX3-	NEGATIVE TRANSMISSION DATA OF PIXEL3
11	RX3+	POSITIVE TRANSMISSION DATA OF PIXEL3
12	NC	NON CONNECTION
13	NC	NON CONNECTION
14	VSS	GROUND
15	NC	NON CONNECTION
16	NC	NON CONNECTION
17	VSS	GROUND
18	NC _	NON CONNECTION
19	NC	NON CONNECTION
20	NC	NON CONNECTION
21	NC	NON CONNECTION
22	NC	NON CONNECTION
23	NC	NON CONNECTION
24	VSS	GROUND
25	NC	NON CONNECTION
26	NC	NON CONNECTION
27	NC	NON CONNECTION
28	VDD	POWER SUPPLY VOLTAGE
29	VDD 🕗	POWER SUPPLY VOLTAGE
30	VDD	POWER SUPPLY VOLTAGE

10.2 CN2

PIN NO.	SYMBOL	FUNCTION
1	VBL+	POWER SUPPLY VOLTAGE FOR LED DRIVER(+)
2	VBL+	POWER SUPPLY VOLTAGE FOR LED DRIVER(+)
3	VBL+	POWER SUPPLY VOLTAGE FOR LED DRIVER(+)
4	VBL-	POWER SUPPLY VOLTAGE FOR LED DRIVER(-)
5	VBL-	POWER SUPPLY VOLTAGE FOR LED DRIVER(-)
6	VBL-	POWER SUPPLY VOLTAGE FOR LED DRIVER(-)
7	BL_EN	BACKLIGHT LED ON/OFF CONTROL
8	BL_PWM	BACKLIGHT LED BRIGHTNESS CONTROL

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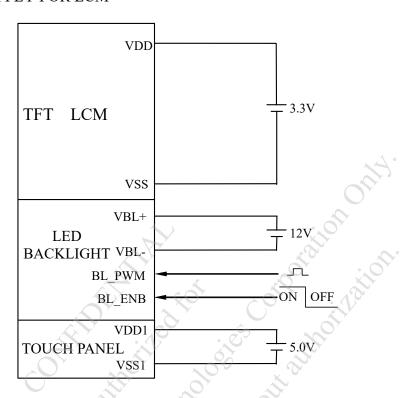
10.3 CN3(USB 2.0)

PIN NO.	SYMBOL	FUNCTION
A1	VSS1	GROUND
A2	NC	NON CONNECTION
A3	NC	NON CONNECTION
A4	VDD1	POWER SUPPLY VOLTAGE FOR TFT
A5	NC	NON CONNECTION
A6	USB DP	USB D+
A7	USB DM	USB D-
A8	NC	NON CONNECTION
A9	VDD1	POWER SUPPLY VOLTAGE FOR TFT
A10	NC	NON CONNECTION
A11	NC	NON CONNECTION
A12	VSS1	GROUND
B1	VSS1	GROUND
B2	NC	NON CONNECTION
В3	NC	NON CONNECTION
B4	VDD1	POWER SUPPLY VOLTAGE FOR TFT
В5	NC .	NON CONNECTION
В6	USB DP	USB D+
В7	USB DM	USB D-
В8	NC	NON CONNECTION
В9	VDD1	POWER SUPPLY VOLTAGE FOR TFT
B10	NC	NON CONNECTION
B11	NC	NON CONNECTION
B12	VSS1	GROUND
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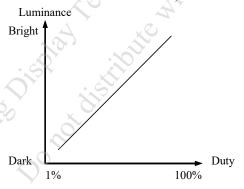
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11. POWER SUPPLY

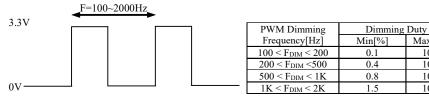
11.1 POWER SUPPLY FOR LCM



NOTE (1) : ADJUST THE PWM SIGNAL IN ORDER TO CONTROL LED BACKLIGHT'S BRIGHTNESS. THE HIGHER THE DUTY CYCLE, THE HIGHER THE BRIGHTNESS



NOTE (2): PWM SIGNAL OPERATION FREQUENCY IS 100~2000Hz AND DIMMING DUTY.



100

100

100

100

EMERGING DISPLAY

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TECHNOLOGIES CORPORATION

12. CAPACITIVE TOUCH PANEL SPECIFICATION

12.1 OPTICAL CHARACTERISTICS

ITEM	CONDITION	MIN.	TYP.	MAX.	UNIT
TRANSPARENCY	$Ta = 25^{\circ}C$	(05)			0/
NOTE (1)	λ=550 nm	(85)	_		%

MODEL NO.

NOTE (1): OPTICAL MEASUREMENT SHOULD BE EXECUTED AFTER PANEL IS SECURED.

MEASUREMENT PROCESS SHOULD BE EXECUTED IN A STABLE, WINDLESS, AND DARK ROOM.

OPTICAL SPECIFICATIONS SHOULD BE MEASURED BY SPECTROPHOTOMETER.

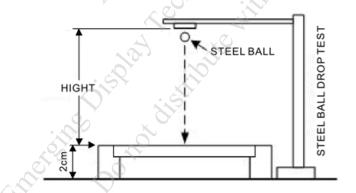
12.2 HARDNESS

ITEM	DESCRIPTION
SURFACE HARDNESS	(7)H (MIN.)

12.3 DURABILITY

USING STEEL BALL AND FALLING ON TOUCH PANEL SURFACE, FROM THE HEIGHT MUST PASS BELOW CONDITIONS:

ITEM	CONDITION	INSPECTION METHOD	DESCRIPTION
STEEL BALL	WEIGHT: 67g	. 0	SIGN OF FRACTURE OR DAMAGE IS NOT ACCEPTABLE
DROP TEST	HEIGHT OF FALL: 30 cm	INSPECTION	3 TIME/ 1 POINTS, 25°C
			(CENTER POINT)



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13. INSPECTION CRITERIA

13.1 APPLICATION

THIS INSPECTION STANDARD IS TO BE APPLIED TO THE LCD MODULE DELIVERED FROM EMERGING DISPLAY TECHNOLOGIES CORP.(E.D.T) TO CUSTOMERS

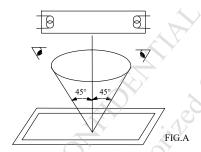
13.2 INSPECTION CONDITIONS

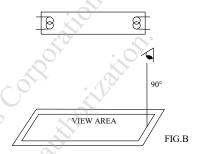
13.2.1 (1)OBSERVATION DISTANCE: 45±5cm

(2) VIEWING ANGLE: ±45°

±45° (FOR SECTION WITHIN VIEWING AREA), REFER TO FIG.A 90° (FOR SECTION OUTSIDE OF VIEWING AREA), REF TO FIG.B PERPENDICULAR TO MODULE SURFACE

VIEWING ANGLE SHOULD BE SMALLER THAN 45°





THE INSPECTION CRITERIA IS ACCORDING TO LINE OF SIGHT. INSPECTION SHALL BE MADE WITHIN THE HALF SECTION OF THE VIEWING CONE GENERATED BY LINE SEGMENT OF 45° WITH RESPECT TO THE VERTICAL AXIS FROM CENTER VERTEX OF LCD, THE FLUORESCENT LAMP AND THE CONE AXIS MUST BE PERPENDICULAR TO THE LCD SURFACE.

IF THE DEFECTS ARE OUTSIDE OF VIEWING AREA, IT SHALL BE INSPECTED BY 90° WITH RESPECT TO THE VERTICAL AXIS FROM EDGE OF VIEWING AREA.

13.2.2 ENVIRONMENT CONDITIONS:

AMBIE	25±5°C	
AMB	$65 \pm 20\%$ RH	
AMBIENT	COSMETIC INSPECTION	600~800 lux
ILLUMINATION	FUNCTIONAL INSPECTION	300~500 lux
INS	15 secs	

13.2.3 INSPECTION LOT QUANTITY PER DELIVERY LOT FOR EACH MODEL

13.2.4 INSPECTION METHOD

A SAMPLING INSPECTION SHALL BE MADE ACCORDING TO THE FOLLOWING PROVISIONS TO JUDGE THE ACCEPTABILITY (a)APPLICABLE STANDARD:

ANSI/ ASQ Z1.4 NORMAL INSPECTION LEVEL II

(b)AQL: MAJOR DEFECT: AQL 0.65 MINOR DEFECT: AQL 1.0

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13.3 INSPECTION STANDARDS

13.3.1 VISUAL DEFECTS CLASSIFICATION

TYPE OF DEFECT	INSPECTION ITEM	DEFECT FEATURE	AQL
	1.DISPLAY ON	DEFECT TO MISS SPECIFIED DISPLAY FUNCTION, FOR ALL AND SPECIFIED DOTS EX: DISCONNECTION, SHORT CIRCUIT ETC	
MAJOR DEFECT	2.CTP FUNCTION	NO FUNCTIONBROKEN LINEFALSE TOUCH	0.65
	3.BACKLIGHT	NO LIGHT FLICKERING AND OTHER ABNORMAL ILLUMINATION SUBJECT TO INDIVIDUAL ACCEPTANCE SPECIFICATIONS	
	4.DIMENSIONS	SUBJECT TO INDIVIDUAL ACCEPTANCE SPECIFICATIONS	
	1.DISPLAY ZONE	 BLACK/WHITE SPOT BUBBLES ON POLARIZER NEWTON RING BLACK/WHITE LINE SCRATCH CONTAMINATION LEVER COLOR SPREAD 	
MINOR DEFECT	2.BEZEL ZONE	STAINSSCRATCHESFOREIGN MATTER	1.0
	3.SOLDERING	 INSUFFICIENT SOLDER SOLDERED IN INCORRECT POSITION CONVEX SOLDERING SPOT SOLDER BALLS SOLDER SCRAPS 	
	4.DISPLAY ON (ALL ON)	• LIGHT LINE	

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13.3.2 MODULE DEFECTS CLASSIFICATION

NO.	ITEM	C	RITERIA	
1	DISPLAY ON INSPECTION	1.INCORRECT PATTERN 2.MISSING SEGMENT 3.DIM SEGMENT 4.OPERATING VOLTAGE BEYOND SPEC		
2	OVERALL DIMENSIONS	1.OVERALL DIMENSION BEYON	D SPEC	
3	DOT DEFECT	1.INSPECTION PATTERN: FULL WHITE, FULL BLACK, RED, GREEN AND BLUE SCREENS. 2. ITEMS ACCEPTABLE COUNT BRIGHT DOT N≤1 DARK DOT TOTAL BRIGHT AND DARK DOTS N≤4 NOTE: (1)THE DEFINITION OF DOT: THE SIZE OF A DEFECTIVE DOT OVER 1/2 OF WHOLE DOT IS REGARDED AS ONE DEFECTIVE DOT. (2)BRIGHT DOT: DOTS APPEAR BRIGHT AND UNCHANGED IN SIZE IN WHICH LCD PANEL IS DISPLAYING UNDER BLACK PATTERN. THE BRIGHT DOT DEFECT MUST BE VISIBLE THROUGH 5% ND FILTER. (3)DARK DOT: DOTS APPEAR DARK AND UNCHANGED IN SIZE IN WHICH LCD PANEL IS DISPLAYING UNDER PURE RED, GREEN, BLUE PICTURI		
4	BUBBLES ON POLARIZER /SURFACE STAINS /DIRT/CF FAIL/SPOT			

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).	ITEM			CRITERIA	
	11EW	THE FOLLOWING B	RI ACK/WHITE SDA	OT ARE WITHIN THE	
		VIEWING AREA. A'			
			E D	PERMISSIBLE NO.	/ D
	DI ACK/WHITE	D≤		IGNORE	D / B
5	BLACK/WHITE			5	
)	SPOT/DENT CIRCULAR TYPE		D≤0.3	5	
	CIRCULAR I II E		<u>0≤0.5</u>	0	
			0.5	Ü	/
		NOTE (1): THE I			
			LD BE MORE THA		
				IIN THE VIEWING AREA.	
		WIDTH: W (mm), I		DED MISSIPLE NO	
			W & L	PERMISSIBLE NO.	
_	COD A TOLL		0.07	IGNORE	
5	SCRATCH		≤0.1, L≤8	4	
			.1, 8 <l≤10< td=""><td>3</td><td></td></l≤10<>	3	
		W>		0	
		NOTE (1): THE I			y
		1	LD BE MORE THA		
				TE LINE IS WITHIN THE	~
		VIEWING AREA. W			:.0
	BLACK /		W & L	PERMISSIBLE NO.	L -
	WHITE LINE		0.07	IGNORE	
7	LINEAR TYPE /		≤0.1, L≤8	4	/ \ \ \\
	FOREIGN FIBER		.1, 8 <l≤10< td=""><td>3</td><td></td></l≤10<>	3	
			0.1	0	
		NOTE (1): THE I			
			LD BE MORE THA	AN 10mm APART.	
		BUBBLES WITHIN			
		AVERAGE DIAMET			
		SIZ		PERMISSIBLE NO.	D 🗸
	BUBBLE / DENT	D≤		IGNORE	
8	FOR OPTICAL		0≤0.3	3	
	BONDING		0≤0.5	∑ 2	
			0.5	0	
		$\overline{\text{NOTE}(1)}$: THE I			
			LD BE MORE THA		
				AGE DIAMETER : D (mm)	1
			E D	PERMISSIBLE NO.	
		D≤		IGNORE	
9	PIN HOLE	- K - K - Z	D≤0.2	5	
		0.2 <i< td=""><td></td><td>2</td><td></td></i<>		2	
		D>		0	
	_	NOTE (1): THE I			
		SHOU	LD BE MORE THA	AN 10mm APART.	
			37	W 42	Chip of glass
		CORNER		$Y \le 3mm \cdot Z < t$	v
	CHIPPING		· · · · · · · · · · · · · · · · · · ·	HICKNESS)	
0		1 1	X < 6mm	$Y \le 1 \text{mm}, Z \le t$	
0		EDGE			×(2)/> '
0		EDGE		HICKNESS)	N. X.

EMERGING DISPLAY

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NO.	ITEM	CRITERIA
12	LINE DEFECT ON DISPLAY	OBVIOUS VERTICAL OR HORIZONTAL LINE DEFECT IS NOT ALLOWED
13	MURA ON DISPLAY	IT'S OK IF MURA IS SLIGHT VISIBLE THROUGH 5% ND FILTER
14	UNEVEN COLOR SPREAD, COLORATION	TO BE DETERMINED BASED UPON THE LIMITED SAMPLE.
15	BEZEL APPEARANCE	(1)BEZEL MAY NOT HAVE RUST, BE DEFORMED OR HAVE FINGER PRINTS STAINS OF OTHER CONTAMINATION. (2)BEZEL MUST COMPLY WITH JOB SPECIFICATIONS.
16	РСВ	 (1)THERE MAY NOT BE MORE THAN 2mm OF SEALANT OUTSIDE THE SEAL AREA ON THE PCB, AND THERE SHOULD BE NO MORE THAN THREE PLACES. (2)NO OXIDATION OR CONTAMINATION PCB TERMINALS. (3)PARTS ON PCB MUST BE THE SAME AS ON THE PRODUCTION CHARACTERISTIC CHART. THERE SHOULD BE NO WRONG PARTS, MISSING PARTS OR EXCESS PARTS. (4)THE JUMPER ON THE PCB SHOULD CONFORM TO THE PRODUCT CHARACTERISTIC CHART. (5)IF SOLDER GETS ON BEZEL TAB PADS, LED PAD, ZEBRA PAD OR SCREW HOLD PAD; MAKE SURE IT IS SMOOTHED DOWN.
17	SOLDERING	(1)NO SOLDERING FOUND ON THE SPECIFIED PLACE (2)INSUFFICIENT SOLDER (a)LSI, IC A POOR WETTING OF SOLDER IS BETWEEN LOWER BEND OR "HEEL" OF LEAD AND PAD SOLDER FILLET SOLDER IS LESS THAN 50% OF SIDES AND FRONT FACE WETTING SOLDER FILLET 1/2 • SOLDER WETS 3 SIDES OF TERMINAL, BUT LESS THAN 25% OF SIDES AND FRONT SURFACE AREA ARE COVERED SOLDER SOLDER

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NO.	ITEM	CRITERIA
17	SOLDERING	(4)NO UNMELTED SOLDER PASTE MAY BE PRESENT ON THE PCB. (5)NO COLD SOLDER JOINTS, MISSING SOLDER CONNECTIONS, OXIDATION OR ICICLE. (6)NO RESIDUE OR SOLDER BALLS ON PCB.
18	BACKLIGHT	(7)NO SHORT CIRCUITS IN COMPONENTS ON PCB. (1)NO LIGHT (2)FLICKERING AND OTHER ABNORMAL ILLUMINATION (3)SPOTS OR SCRATCHES THAT APPEAR WHEN LIT MUST BE JUDGED USING LCD SPOT, LINES AND CONTAMINATION STANDARDS. (4)BACKLIGHT DOESN'T LIGHT OR COLOR IS WRONG.
	GENERAL APPEARANCE	 (1)NO OXIDATION, CONTAMINATION, CURVES OR, BENDS ON INTERFACE PIN (OLB) OF TCP. (2)NO CRACKS ON INTERFACE PIN (OLB) OF TCP. (3)NO CONTAMINATION, SOLDER RESIDUE OR SOLDER BALLS ON PRODUCT. (4)THE IC ON THE TCP MAY NOT BE DAMAGED, CIRCUITS. (5)THE UPPERMOST EDGE OF THE PROTECTIVE STRIP ON THE INTERFACE PIN MUST BE PRESENT OR LOOK AS IF IT CAUSE THE INTERFACE PIN TO SEVER. (6)THE RESIDUAL ROSIN OR TIN OIL OF SOLDERING (COMPONENT OR CHIP COMPONENT) IS NOT BURNED INTO BROWN OR BLACK COLOR. (7)SEALANT ON TOP OF THE ITO CIRCUIT HAS NOT HARDENED. (8)PIN TYPE MUST MATCH TYPE IN SPECIFICATION SHEET. (9)LCD PIN LOOSE OR MISSING PINS. (10)PRODUCT PACKAGING MUST BE THE SAME AS SPECIFIED ON PACKAGING SPECIFICATION SHEET. (11)PRODUCT DIMENSION AND STRUCTURE MUST CONFORM TO PRODUCT SPECIFICATION SHEET. (12)THE APPEARANCE OF HEAT SEAL SHOULD NOT ADMIT ANY DIRT AND BREAK.

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14. RELIABILITY TEST

14.1 STANDARD SPECIFICATIONS FOR RELIABILITY OF LCD MODULE

NO.	ITEM	DESCRIPTION	
1	HIGH TEMPERATURE TEST (OPERATION)	THE SAMPLE SHOULD BE ALLOWED TO STAND AT +85°C FOR 240 HRS	
2	LOW TEMPERATURE TEST (OPERATION)	THE SAMPLE SHOULD BE ALLOWED TO STAND AT -30°C FOR 240 HRS	
3	HIGH TEMPERATURE TEST (STORAGE)	THE SAMPLE SHOULD BE ALLOWED TO STAND AT +85°C FOR 240 HRS	
4	LOW TEMPERATURE TEST (STORAGE)	THE SAMPLE SHOULD BE ALLOWED TO STAND AT -40°C FOR 240 HRS	
5	HUMIDITY TEST	THE SAMPLE SHOULD BE ALLOWED TO STAND AT 60°C, 90% RH 240 HRS	
6	THERMAL SHOCK (NOT OPERATED)	THE SAMPLE SHOULD BE ALLOWED TO STAND THE FOLLOWING 10 CYCLES OF OPERATION: +85°C 40°C 30 min 3 min 30 min 30 min 3 min 30 min 30 min 30 min 3 min 30	
7	ESD (ELECTROSTATIC DISCHARGE) (NOT OPERATED)	AIR DISCHARGE ± 12KV CONTACT DISCHARGE ± 8KV (ACCORDING TO IEC-61000-4-2)	

NOTE (1): THE TEST SAMPLES HAVE RECOVERY TIME FOR 2 HOURS AT ROOM TEMPERATURE BEFORE THE FUNCTION CHECK. IN THE STANDARD CONDITIONS, THERE IS NO DISPLAY FUNCTION NG ISSUE OCCURRED.

NOTE (2): WHEN THE LCD MODULE IS OPERATED AT A HIGHER AMBIENT TEMPERATURE THAN 60°C, THE PWM DUTY CYCLE OF THE LED BACKLIGHT SHOULD BE ADJUSTED TO BE LESS THAN (TBD)%. IF THE MODULE IS OPERATED AT A HIGHER DUTY CYCLE THAN (TBD)%, THEN THERE IS A POSSIBILITY OF DISTORTION AND IRREGULARITY OF THE PICTURE DUE TO LIQUID CRYSTAL BEHAVIOR.

NOTE (3): TESTING CONDITIONS AND INSPECTION CRITERIA

]	NO.	ITEM	TEST MODEL	INSPECTION CRITERIA
	1	CURRENT		THE CURRENT CONSUMPTION SHOULD
	1	CONSUMPTION	REFER TO SI ECIFICATION	CONFORM TO THE PRODUCT SPECIFICATION.
		CONTRAST R		AFTER THE TESTS HAVE BEEN EXECUTED,
	2			THE CONTRAST MUST BE LARGER THAN HALF
				OF ITS INITIAL VALUE PRIOR TO THE TESTS.
	3	APPEARANCE	VISUAL INSPECTION	DEFECT FREE

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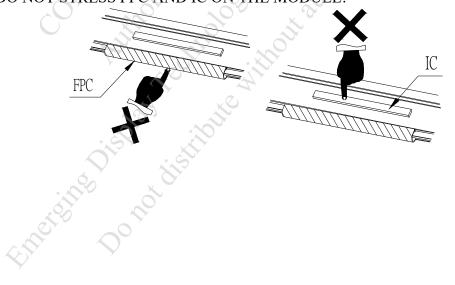
15. CAUTION

15.1 OPERATION

- 15.1.1 DO NOT CONNECT OR DISCONNECT MODULES TO OR FROM THE MAIN SYSTEM WHILE POWER IS BEING SUPPLIED .
- 15.1.2 USE THE MODULE WITHIN SPECIFIED TEMPERATURE; LOWER TEMPERATURE CAUSES THE RETARDATION OF BLINKING SPEED OF THE DISPLAY; HIGHER TEMPERATURE MAKES OVERALL DISPLAY DISCOLOR. WHEN THE TEMPERATURE RETURNS TO NORMALITY, THE DISPLAY WILL OPERATE NORMALLY.
- 15.1.3 ADJUST THE LC DRIVING VOLTAGE TO OBTAIN THE OPTIMUM CONTRAST .
- 15.1.4 POWER ON SEQUENCE INPUT SIGNALS SHOULD NOT BE SUPPLIED TO LCD MODULE BEFORE POWER SUPPLY VOLTAGE IS APPLIED AND REACHES THE SPECIFIED VALUE.

 IF ABOVE SEQUENCE IS NOT FOLLOWED, CMOS LSIS OF LCD MODULES MAY BE DAMAGED DUE TO LATCH UP PROBLEM.
- 15.1.5 NOT ALLOWED TO INFLICT ANY EXTERNAL STRESS AND TO CAUSE ANY MECHANICAL INTERFERENCE ON THE BENDING AREA OF FPC DURING THE TAIL BENDING BACKWARDS!

 DO NOT STRESS FPC AND IC ON THE MODULE!



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15.2 HANDLING

- 15.2.1 USE A GROUNDED SOLDERING IRON WHEN SOLDERING CONNECTOR I/O TERMINALS . FOR SOLDERING OR REPAIRING, TAKE PRECAUTION AGAINST THE TEMPERATURE OF THE SOLDERING IRON AND THE SOLDERING TIME TO PREVENT PEELING OFF THE THROUGH-HOLE-PAD.
- 15.2.2 DO NOT DISASSEMBLE . EDT SHALL NOT BE HELD RESPONSIBLE IF THE MODULE IS DISASSEMBLED AND UPON THE REASSEMBLY THE MODULE FAILED.
- 15.2.3 DO NOT CHARGE STATIC ELECTRICITY, AS THE CIRCUIT OF THIS MODULE CONTAINS CMOS LSIS. A WORKMAN'S BODY SHOULD ALWAYS BE STATIC-PROTECTED BY USE OF AN ESD STRAP. WORKING CLOTHES FOR SUCH PERSONNEL SHOULD BE OF STATIC-PROTECTED MATERIAL.
- 15.2.4 ALWAYS GROUND THE ELECTRICALLY-POWERED DRIVER BEFORE USING IT TO INSTALL THE LCD MODULE. WHILE CLEANING THE WORK STATION BY VACUUM CLEANER, DO NOT BRING THE SUCKING MOUTH NEAR THE MODULE; STATIC ELECTRICITY OF THE ELECTRICALLY-POWERED DRIVER OR THE VACUUM CLEANER MAY DESTROY THE MODULE.
- 15.2.5 DON'T GIVE EXTERNAL SHOCK.
- 15.2.6 DON'T APPLY EXCESSIVE FORCE ON THE SURFACE.
- 15.2.7 LIQUID IN LCD IS HAZARDOUS SUBSTANCE. MUST NOT LICK AND SWALLOW.

 WHEN THE LIQUID IS ATTACH TO YOUR, SKIN, CLOTH ETC. WASH IT OUT THOROUGHLY AND IMMEDIATELY.
- 15.2.8 DON'T OPERATE IT ABOVE THE ABSOLUTE MAXIMUM RATING.
- 15.2.9 STORAGE IN A CLEAN ENVIRONMENT, FREE FROM DUST, ACTIVE GAS, AND SOLVENT.
- 15.2.10 STORE WITHOUT ANY PHYSICAL LOAD.
- 15.2.11 REWIRING: NO MORE THAN 3 TIMES.