MINED BY:		FILE NO . CAS-0009520
Sam Chou	EMERGING DISPLAY	ISSUE : JAN.29, 2024
OVED BY:	TECHNOLOGIES CORPORATION	TOTAL PAGE: 29
Elvis Wu		VERSION: 5
CUSTOMER	ACCEPTANCE SPEC	CIFICATIONS
FOR	Red Dollar Stribute Striban St	stion dition.

EMERG	ING D	ISPLAY	MODEL NO.		VERSION	PAGE		
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RECORD	S OF R	EVISION			M	AR.30, 2023		
DATE	REVISED PAGE NO.		SU	MMARY				
MAY.10, 2023	1	PLEASE REFE		E TOUCH PANEL CO	ONTROLLE.	R/DRIVER		
	3	3.3 ENVIRONME	.3 ENVIRONMENTAL ABSOLUTE MAXIMUM RATINGS					
	13	8. BLOCK DIAGI	AMBIENT TEMPERATURE: STORAGE: MIN.= -30°C → - 40°C  B. BLOCK DIAGRAM  PIN.15: NC→RESET; PIN.16: NC→STBYB					
	15	10.1 TFT LCM (CI	N1)	FUNCTION	4			
		15   NC   16   NC     PIN NO.   SYMBU   15   RESE   15   RESE   16   RESE   16   RESE   17   RESE	T RESET PIN	FUNCTION				
	26	NO. ITEM  LOW TEMPERATU	SPECIFICATIONS  JRE THE SAMPLE SHOULD BE A	FOR RELIABILITY DESCRIPTION ALLOWED TO STAND AT -30°C FOR 2-		DDULE		
		THERMAL SHO	THE SAMPLE SHOULD BE A 10 CYCLES OF OPERATION: +85°C	LLOWED TO STAND THE FOLLOWIN	iG			
		(NOT OPERATE	D) 30 min 5 min 30 min 1CYCLE	S min				
		NO. ITEM  4 LOW TEMPERATU TEST (STORAG	JRE THE SAMBLE SHOULD BE A	DESCRIPTION ALLOWED TO STAND AT -40°C FOR 24	40 HRS			
		6 THERMAL SHO (NOT OPERATE	10 CYCLES OF OPERATION: +85°C	LLOWED TO STAND THE FOLLOWIN	īG			
SEP.22, 2023	1	1.1 DATA SHEETS PLEASE REFER SITRONIX ST58 SITRONIX ST50	FOR LCD PANEL CO TO: 21DG	→ ONTROLLER/DRIVER				
		FITIPOWER EK		SPECIFICATIONS				
		0						
	2		TOUCH PANEL M	MECHANICAL SPECI 137.6H mm→217.96				
	3	3.1 LCD MODULE I	ELECTRICAL ABSO SYMBOL MIN.	LUTE MAXIMUM RAT	ΓINGS			
		POWER SUPPLY VOLTA	GE VDD-VSS -0.3	5.0 V				
		ITEM POWER SUPPLY VOLTA	SYMBOL MIN.  GE VDD-VSS -0.5	MAX. UNIT REMARK 4.0 V				
		3.3 ENVIRONMENTA NOTE(3): Ta≤60 Ta > 6  WET I	L ABSOLUTE MAXIM °C: 90%RH MAX. (96 0°C: ABSOLUTE HUN OF 90%RH AT 66 BULB TEMPERATURE	UM RATINGS	HAN 57.8°C, AN	ND NO		

#### MODEL NO. VERSION **PAGE** EMERGING DISPLAY TECHNOLOGIES CORPORATION ETML101025LDYA 5 0-2DOC . FIRST ISSUE SEP.22, 2023 RECORDS OF REVISION REVISED DATE **PAGE** SUMMARY NO. SEP.22, 2023 4 4.1 LCD MODULE ELECTRICAL CHARACTERISTICS ITEM SYMBOL MIN. MAX UNIT REMARK LOGIC HIGH INPUT VOLTAGE VIH 0.7VDD VDD v LOGIC LOW INPUT VOLTAGE VDD-0.4 LOGIC HIGH OUTPUT VOLTAGE VOH LOGIC LOW OUTPUT VOLTAGE VOL ITEM SYMBOL CONDITION MIN. MAX UNIT REMARK LOGIC HIGH INPUT VOLTAGE VIH 0.8VDD VDD LOGIC LOW INPUT VOLTAGE VIL 0.2VDD LOGIC HIGH OUTPUT VOLTAGE LOGIC LOW OUTPUT VOLTAGE VOH 0.8VDD VDD 6 5.1 LVDS INTERFACE →5.1 LVDS DC ELECTRICAL CHARACTERISTICS 7 5.2 POWER ON/OFF SEQUENCE → 5.2 LVDS MODE AC ELECTRICAL CHARACTERISTICS 8 5.3 CAPACITIVE TOUCH PANEL I2C INTERFACE TIMING CHARACTERISTICS →5.3 LVDS INTERFACE 9 ADD 5.4 DATA INPUT FORMAT FOR LVDS 5.5 POWER ON/OFF SEQUENCE 10 ADD 5.6 CAPACITIVE TOUCH PANEL 12C INTERFACE TIMING **CHARACTERISTICS** 6.1 OPTICAL CHARACTERISTICS 11 ITEM CONTRAST RATIO (CENTER) SYMBOL MIN. TYP. MAX. UNIT REMARK NOTE (3) (600)(800)T<sub>R</sub>(rise) +T<sub>F</sub>(fall) RESPONSE TIME 40 NOTE (4) (0.38) (0.28) (0.33) WHITE θx=0°, θy=0° VDD-VSS=3.3V VBL+-VBL-=12V COLOR (0.61) RED Ry (0.30) (0.35) (0.40) CHROMATICITY NOTE (5) (0.37) (0.42) GREEN LED B/L=ON PWM=100% (CENTER) BLUE ITEM CONTRAST RATIO (CENTER) SYMBOL CONDITION CR (800) (1000) NOTE (3) T<sub>R</sub>(rise) +T<sub>F</sub>(fall) RESPONSE TIME 25 NOTE (4) (0.26) (0.31) (0.36) (0.29) (0.34) (0.39) (0.54) (0.59) (0.64) (0.31) (0.36) (0.41) (0.28) (0.33) (0.38) (0.53) (0.58) (0.63) (0.11) (0.16) (0.21) WHITE θx=0°, θy=0° VDD-VSS=3.3V VBL+-VBL=12V LED B/L=ON PWM=100% CHROMATICITY NOTE (5 GREEN (CENTER) (0.11) (0.16) (0.21)7. OUTLINE DIMENSIONS 13 MARK ⚠: 1.MODIFY BM TAPE OUTLINE 2.MODIFY V.A. & THICKNESS DIMENSION & TFT PCB->FPC & NOTE 8. BLOCK DIAGRAM 14 TFT LCD PANEL 1280(RGB)\*800 DDTS TFT LCD PANEL 1280(RGB)\*800 DUTS LED BACKLIGHT LED BACKLIGHT SOURCE DRIVER FPC 11 DRIVERING BOARD PCB VGL, VGH, T-CON VCDN, VDD, AVDD GAMMA LED VERING BOARD SEE PROGRAMMA SERSAMA A BARATA A BARATA

## MODEL NO. VERSION PAGE EMERGING DISPLAY TECHNOLOGIES CORPORATION ETML101025LDYA 5 0 - 3DOC . FIRST ISSUE SEP.22, 2023 RECORDS OF REVISION REVISED DATE **PAGE** SUMMARY NO. SEP.22, 2023 16 10.1 TFT LCM (CN1) 21 13.2.4 INSPECTION METHOD MINOR DEFECT: AQL 1.0→AQL 1.5 22 13.3 DEFECTS CLASSIFICATION MINOR DEFECT: AQL 1.0→AQL 1.5 13.3 DEFECTS CLASSIFICATION 23,24 OCT.23, 2023 2. MECHANICAL SPECIFICATIONS (8)LCD TYPE:TFT, IPS, TRANSMISSIVE, NORMALLY BLACK→ TFT, IPS, TRANSMISSIVE, NORMALLY BLACK, **ANTI-GLARE** 11 6.1 OPTICAL CHARACTERISTICS ITEM:CONTRAST RATIO(CENTER),MIN.:(800)→(600),TYP.:(1000)→(800) 14 8. BLOCK DIAGRAM TFT LCD PANEL 1280(RGB)\*800 DDTS TFT LCD PANEL 1280(RGB)\*800 DOTS LED BACKLIGHT LED DRIVERING BOARD

#### MODEL NO. VERSION **PAGE** EMERGING DISPLAY TECHNOLOGIES CORPORATION ETML101025LDYA 5 0-4DOC . FIRST ISSUE SEP.22, 2023 RECORDS OF REVISION REVISED DATE **PAGE** SUMMARY NO. OCT.23, 2023 16 10.1 TFT LCM (CN1) PIN NO. SYMBOL FUNCTION NON CONNECTION NEGATIVE TRANSMISSION DATA OF PIXEL0 RX0-RX0+ POSITIVE TRANSMISSION DATA OF PIXEL0 4 VSS GROUND RX1-NEGATIVE TRANSMISSION DATA OF PIXEL1 POSITIVE TRANSMISSION DATA OF PIXEL1 6 RX1+8 RX2-NEGATIVE TRANSMISSION DATA OF PIXEL2 9 RX2+ POSITIVE TRANSMISSION DATA OF PIXEL2 10 VSS GROUND NEGATIVE OF CLOCK 11 RXCLK-12 RXCLK+ POSITIVE OF CLOCK 13 VSS GROUND 14 NEGATIVE TRANSMISSION DATA OF PIXEL3 RX3-POSITIVE TRANSMISSION DATA OF PIXEL3 15 RX3+ 16 VSS GROUND NON CONNECTION 17 NC 19 VSS GROUND VSS GROUND 22 23 VSS GROUND 24 NC NON CONNECTION POWER SUPPLY VOLTAGE VDD 30 NON CONNECTION NC $\mathcal{S}_{\downarrow}$ PIN NO. SYMBOL **FUNCTION** NEGATIVE TRANSMISSION DATA OF PIXELO 1 RX0-RX0+ POSITIVE TRANSMISSION DATA OF PIXEL0 NEGATIVE TRANSMISSION DATA OF PIXEL1 RX1-POSITIVE TRANSMISSION DATA OF PIXEL1 RX1+ RX2-NEGATIVE TRANSMISSION DATA OF PIXEL2 6 RX2+ POSITIVE TRANSMISSION DATA OF PIXEL2 NEGATIVE OF CLOCK **RXCLK** 9 POSITIVE OF CLOCK RXCLK+ NEGATIVE TRANSMISSION DATA OF PIXEL3 10 RX3-11 RX3+ POSITIVE TRANSMISSION DATA OF PIXEL3 12 NC NON CONNECTION NON CONNECTION 13 NC 14 VSS GROUND 15 NC NON CONNECTION NON CONNECTION 16 NC GROUND 17 VSS NON CONNECTION 19 NC 22 NC NON CONNECTION 23 NON CONNECTION NC VSS 24 GROUND NC NON CONNECTION 30 VDD POWER SUPPLY VOLTAGE JAN.29, 2024 2.1 TFT LCD MODULE MECHANICAL SPECIFICATIONS (13)WEIGHT:TBD $\rightarrow$ 462g 4 4.1 LCD MODULE ELECTRICAL CHARACTERISTICS ITEM MIN. TYP. MAX. POWER SUPPLY CURRENT TBD TBD POWER SUPPLY VOLTAGE (11.5)(12.0)(12.5)FOR LED DRIVER POWER SUPPLY CURRENT TBD TBD FOR LED DRIVER ITEM MIN. TYP. MAX. POWER SUPPLY CURRENT 360 470 POWER SUPPLY VOLTAGE 11.5 12.0 12.5 FOR LED DRIVER POWER SUPPLY CURRENT 810 1050 FOR LED DRIVER

#### MODEL NO. VERSION **PAGE** EMERGING DISPLAY TECHNOLOGIES CORPORATION ETML101025LDYA 5 0-5DOC . FIRST ISSUE SEP.22, 2023 RECORDS OF REVISION REVISED DATE **PAGE** SUMMARY NO. JAN.29, 2024 5 4.2 CAPACITIVE TOUCH PANEL ELECTRICAL CHARACTERISTICS ITEM:POWER SUPPLY CURRENT, TYP.:(250)→145, MAX.:(325)→190 11 6.1 OPTICAL CHARACTERISTICS SYMBOL MIN. TYP. MAX. SYMBOL MIN. TYP. MAX. ITEM CONTRAST RATIO ITEM CONTRAST RATIO CR (800) CR 600 800 (0.26) (0.31) 0.26 0.31 Wx (0.36) WHITE WHITE Wv (0.34) (0.39) 0.30 0.35 0.40 (0.54) (0.59) Rx Ry 0.55 0.60 0.65 COLOR CHROMATICITY (CENTER) COLOR CHROMATICITY (CENTER) RED RED 0.30 0.35 Ry (0.28)(0.33)(0.38)Gx GREEN GREEN (0.53) (0.58) (0.63) 0.52 BLUE BLUE Ву (0.09)(0.14)(0.19)THE BRIGHTNESS OF THE BRIGHTNESS OF В 1250 1350 В (1250)(1350)MODULE(CENTER) MODULE(CENTER) THE UNIFORMITY OF THE UNIFORMITY OF MODULE 13 7. OUTLINE DIMENSIONS MARK △: MODIFY C/L OUTLINE DIMENSION & BM TAPE OUTLINE & NOTE1 23 13.3 DEFECTS CLASSIFICATION NO.3, ITEM:DOT DEFECT, CRITERIA:2.: ITEMS:BRIGHT DOT: RANDOM, ACCEPTABLE COUNT: N=0→N≤1 ITEMS:TOTAL BRIGHT AND DARK DOT, ACCEPTABLE COUNT: A single distribute without the distribute of th

 MODEL NO.
 VERSION
 PAGE

 ETML101025LDYA
 5
 0-6

## TABLE OF CONTENTS

NO.	ITEM	PAGE
=====		
1.	GENERAL SPECIFICATIONS	1
2.	MECHANICAL SPECIFICATIONS	1,2
3.	ABSOLUTE MAXIMUM RATINGS	3
4.	ELECTRICAL CHARACTERISTICS	4,5
5.	TIMING CHARACTERISTICS	6~10
6.	OPTICAL CHARACTERISTICS	11,12
7.	OUTLINE DIMENSIONS	13
8.	BLOCK DIAGRAM	14
9.	DETAIL DRAWING OF DOT MATRIX	15
10.	INTERFACE SIGNALS	16,17
11.	POWER SUPPLY	18
12.	CAPACITIVE TOUCH PANEL SPECIFICATION	19,20
13.	INSPECTION CRITERIA	$21\sim26$
14.	RELIABILITY TEST	27
15.	CAUTION	28, 29
	Charles Donot distribute	

EMERGING DISPLAY	MODEL NO.	VERSION	PAGE
TECHNOLOGIES CORPORATION	ETML101025LDYA	5	1

### 1. GENERAL SPECIFICATIONS

1.1 DATA SHEETS FOR LCD PANEL CONTROLLER/DRIVER PLEASE REFER TO :

### FITIPOWER EK79202B

1.2 DATA SHEET FOR CAPACITIVE TOUCH PANEL CONTROLLER/DRIVER PLEASE REFER TO:

### **TOUCHNETIX AX80A**

1.3 MATERIAL SAFETY DESCRIPTION
ASSEMBLIES SHALL COMPLY WITH EUROPEAN ROHS REQUIREMENTS,
INCLUDING PROHIBITED MATERIALS/COMPONENTS CONTAINING LEAD,
MERCURY, CADMIUM, HEXAVALENT CHROMIUM, POLYBROMINATED
BIPHENYLS (PBB) AND POLYBROMINATED
DIPHENYL ETHERS (PBDE), BIS(2-ETHYLHEXYL) PHTHALATE (DEHP), BUTYL
BENZYL PHTHALATE (BBP), DIBUTYL PHTHALATE (DBP), DIISOBUTYL
PHTHALATE (DIBP).

### 2. MECHANICAL SPECIFICATIONS

### 2.1 TFT LCD MODULE MECHANICAL SPECIFICATIONS

(1) DISPLAY SIZE	10.1 inch
(2) NUMBER OF DOTS	1280(RGB)W * 800H DOTS
(3) MODULE SIZE	234.2W * 158.2H *14.0D (MAX.) mm
(4) VIEWING AREA	217.96W * 136.6H mm
(5) ACTIVE AREA	216.96W * 135.6H mm
(6) DOT SIZE	0.0565W * 0.1695H mm
(7) PIXEL SIZE	0.1695W * 0.1695H mm
(8) LCD TYPE	TFT, IPS, TRANSMISSIVE,
	NORMALLY BLACK, ANTI-GLARE
(9) COLOR	16.7M
(10) VIEWING DIRECTION	SUPER WIDE VIEW
(11) BACK LIGHT	LED , COLOR : WHITE
( 12 ) INTERFACE MODE	LVDS (8BIT)
(13) WEIGHT	462g

MERGING DISPLAY	MODEL NO.	VERSION	PAG
TECHNOLOGIES CORPORATION	ETML101025LDYA	5	2
2.2 CAPACITIVE TOUCH PANEL M	FCHANICAL SPECIFICATIONS	<u>.</u>	
2.2 CALACITIVE TOOCH TAINEE WI	Lenancal of Len teatrons	•	
(1) TOUCH PANEL SIZE			
(2) OUTER DIMENSION			
(3) VIEWING AREA	(NOT INCLUDED	*	
(4) ACTIVE AREA			
(5) INPUT TYPE		111111	
(6) NUMBER OF TOUCH SENSOR			
(7) RESOLUTION		14.	
(8) INTERFACE MODE			
COLLINATION	7 101		
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MODEL NO.	VERSION	PAGE
ETML101025LDYA	5	3

#### 3. ABSOLUTE MAXIMUM RATINGS

#### 3.1 LCD MODULE ELECTRICAL ABSOLUTE MAXIMUM RATINGS

ITEM	SYMBOL	MIN.	MAX.	UNIT	REMARK
POWER SUPPLY VOLTAGE	VDD-VSS	-0.5	4.0	V	
STATIC ELECTRICITY	_	_	_	V	NOTE (1)
POWER SUPPLY VOLTAGE FOR LED DRIVER	VBL+-VBL-	-0.3	27	V	

NOTE (1): LCM SHOULD BE GROUND DURING LCM HANDLING.

NOTE (2): THE ABSOLUTE MAXIMUM RATING VALUES OF THIS PRODUCT ARE NOT ALLOWED TO BE EXCEEDED AT ANY TIMES. SHOULD A MODULE BE USED WITH ANY OF THE ABSOLUTE MAXIMUM RATINGS EXCEEDED, THE CHARACTERISTICS OF THE MODULE MAY NOT BE RECOVERED, OR IN AN EXTREME CASE, THE MODULE MAY BE PERMANENTLY DESTROYED.

#### 3.2 CAPACITIVE TOUCH PANEL ELECTRICAL ABSOLUTE MAXIMUM RATINGS

ITEM	SYMBOL	MIN.	MAX.	UNIT	REMARK
POWER SUPPLY VOLTAGE	VDD1-VSS1	-0.3	4	V	

### 3.3 ENVIRONMENTAL ABSOLUTE MAXIMUM RATINGS

ITEM	OPERATING		STORAGE		REMARK
TIEWI	MIN.	MAX.	MIN.	MAX.	KEWAKK
AMBIENT TEMPERATURE	-30 °C	85 °C	-40 °C	85 °C	NOTE (1),(2),(3),(4)
HUMIDITY	NOTI	E(3)	NOTE	7	WITHOUT
	11011		NOTE (3)		CONDENSATION
	. 6	$2.45 \text{ m/s}^2$	XO	11.76 m/s <sup>2</sup>	10~100 Hz
VIBRATION	-0	(0.25 G)	5" —	(1.2 G)	XYZ DIRECTIONS
	357	(0.23 0)		(1.2 0)	1 HR EACH
		$29.4 \text{ m/s}^2$		490.0 m/s <sup>2</sup>	10ms
SHOCK	%)—	(3 G)	—	(50 G)	XYZ DIRECTIONS
.62	7	(30)		(300)	1 TIME EACH

- NOTE (1): THE ABSOLUTE MAXIMUM RATINGS OF THIS PRODUCT SHOULD NOT BE EXCEEDED AT ANY TIME. IF THESE RATINGS ARE EXCEEDED, THE PRODUCT'S PERFORMANCE IS NOT GUARANTEED AND THE PRODUCT MAY EXPERIENCE PERMANENT DAMAGE.
- NOTE (2): BACKGROUND COLOR CHANGES SLIGHTLY DEPENDING ON AMBIENT TEMPERATURE THIS PHENOMENON IS REVERSIBLE.
- NOTE (3): WET BULB TEMPERATURE SHOULD BE LOWER THAN 57.8°C, AND NO CONDENSATION OF WATER.
  BESIDES, PROTECT THE MODULE FROM STATIC ELECTRICITY.
- NOTE (4): WHEN THE LCD MODULE IS OPERATED AT A HIGHER AMBIENT TEMPERATURE THAN 60°C, THE PWM DUTY CYCLE OF THE LED BACKLIGHT SHOULD BE ADJUSTED TO BE LESS THAN (TBD)%. IF THE MODULE IS OPERATED AT A HIGHER DUTY CYCLE THAN (TBD)%, THEN THERE IS A POSSIBILITY OF DISTORTION AND IRREGULARITY OF THE PICTURE DUE TO LIQUID CRYSTAL BEHAVIOR.

MODEL NO.	VERSION	PAGE
E T M L 1 0 1 0 2 5 L D Y A	5	4

## 4. ELECTRICAL CHARACTERISTICS

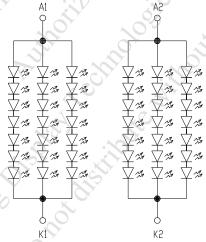
### 4.1 LCD MODULE ELECTRICAL CHARACTERISTICS

Ta = 25 °C

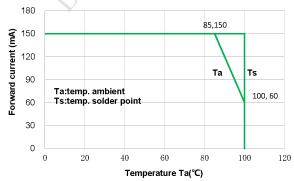
ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	REMARK
POWER SUPPLY VOLTAGE	VDD-VSS	_	3.15	3.3	3.45	V	
LOGIC HIGH INPUT VOLTAGE	VIH	_	0.8VDD	_	VDD	V	
LOGIC LOW INPUT VOLTAGE	VIL	_	VSS	_	0.2VDD	V	
LOGIC HIGH OUTPUT VOLTAGE	VOH	_	0.8VDD	_	VDD	V	
LOGIC LOW OUTPUT VOLTAGE	VOL	_	VSS	_	0.2VDD	V	
POWER SUPPLY CURRENT	IDD	VDD-VSS =3.3V	_	360	470	mA	NOTE (1)
POWER SUPPLY VOLTAGE FOR LED DRIVER	VBL+- VBL-	_	11.5	12.0	12.5	$\beta^{\mathbf{v}}$	NOTE (2)
LOGIC HIGH INPUT VOLTAGE FOR BL_EN, BL_PWM	VIH	_	1.6	_		V	
LOGIC LOW INPUT VOLTAGE FOR BL_EN, BL_PWM	VIL	<u> </u>			0.8	V	
POWER SUPPLY CURRENT FOR LED DRIVER	IBL	VBL+-VBL-=12.0V LED B/L=ON PWM=100%	_	810	1050	mA	
LED LIFE TIME	-	IF=70 mA (PER LED)	50000	2	. 73	HRS	NOTE (4) NOTE (5)

NOTE (1): THE DISPLAY PATTERN IS ALL "WHITE".

NOTE (2): INTERNAL CIRCUIT DIAGRAM OF BACKLIGHT.



NOTE (3): AMBIENT TEMP. VS. ALLOWABLE FORWARD CURRENT.(PER LED)



NOTE (4): CONDITIONS; Ta=25 °C, CONTINUOUS LIGHTING

NOTE (5): DEFINITIONS OF LIFE TIME:

LCM LUMINANCE BECOMES HALF OF THE INITIAL VALUE.

MODEL NO.	VERSION	PAGE
ETML101025LDYA	5	5

### 4.2 CAPACITIVE TOUCH PANEL ELECTRICAL CHARACTERISTICS

 $Ta = 25 \, ^{\circ}C$ 

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	
POWER SUPPLY VOLTAGE	VDD1-VSS1	_	3.15	3.30	3.45	V	
LOGIC HIGH INPUT VOLTAGE	VIH	_	2.0		VDD1	V	
LOGIC LOW INPUT VOLTAGE	VIL	_	-0.3		0.8	V	
LOGIC HIGH OUTPUT VOLTAGE	VOH	_	2.4		VDD1	V	
LOGIC LOW OUTPUT VOLTAGE	VOL	_	0	_	0.4	V	
POWER SUPPLY CURRENT	IDD1	VDD1-VSS1 =3.30V	_	145	190	mA	
POWER SUPPLY CURRENT IDD1 VDD1-VSS1 = 3.30V — 145 190 mA							

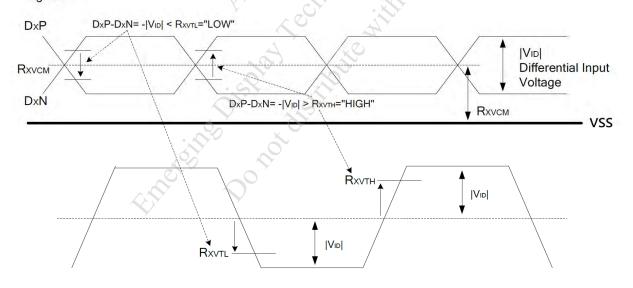
MODEL NO.	VERSION	PAGE
ETML101025LDYA	5	6

## 5. TIMING CHARACTERISTICS

## 5.1 LVDS DC ELECTRICAL CHARACTERISTICS

ITEM	SYMBOL	7	VALUE	ES	UNIT	REMARK
TTEM	SIMBOL	MIN.	TYP.	MAX.	UNII	KEWAKK
DIFFERENTIAL INPUT HIGH	$R_{XVTH}$	_		+0.1	V	
THRESHOLD VOLTAGE	TCAVIH			. 0.1	•	R <sub>XVCM</sub> =1.2V
DIFFERENTIAL INPUT LOW	$R_{ m XVTL}$	-0.1		_	V	TEAVENT 1.2 V
THRESHOLD VOLTAGE	KAVIL	0.1			*	
INPUT VOLTAGE RANGE	R <sub>XVIN</sub>	0.7		1.7	V	
(SINGLED-END)	IXXVIN	0.7		1.7	V	
DIFFERENTIAL INPUT COMMON	D	1	1.2	1.4	V	V <sub>ID</sub>  =0.2
MODE VOLTAGE	$R_{XVCM}$	1	1.2	1.4	·	V ID =0.2
DIFFERENTIAL INPUT	7	80	100	125	ohm	
IMPEDANCE	$Z_{ m ID}$	80	100	123	OIIII	$\mathcal{O}$
DIFFERENTIAL INPUT VOLTAGE	VID	0.2		0.6	V	
DIFFERENTIAL INPUT LEAKAGE	T	-10		+10		
CURRENT	$I_{LCLVDS}$	-10		+10	μΑ	
	, CY				1	FDCLK=80MHz,
LVDS DIGITAL OPERATING		Æ	15	20	mA	VDD=3.3V
CURRENT	$I_{\mathrm{VDD}}$	(0)	13	20	IIIA	INPUT PATTERN:
	DY	Y				55h->Aah->55h->Aah
LVDS DIGITAL STAND-BY	7	<b>Y</b>	S		0	CLOCK & ALL
CURRENT	$I_{ST}$	_		250	μA	FUNCTIONS ARE
CURRENT			2		,	STOPPED

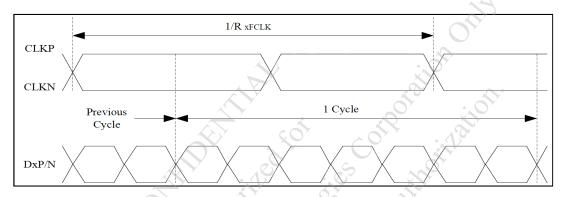


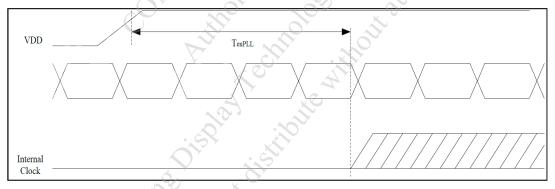


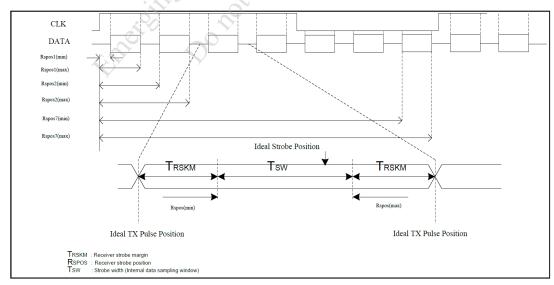
MODEL NO. VERSION PAGE
ETML101025LDYA 5 7

## 5.2 LVDS MODE AC ELECTRICAL CHARACTERISTICS

ITEM	SYMBOL	VALUES			UNIT	REMARK	
I I EIVI	SIMBOL	MIN.	TYP.	MAX.	UNII	KEWIAKK	
CLOCK FREQUENCY	$R_{xFCLK}$	30		TBD	MHz	REFER TO INPUT	
						TIMING TABLE FOR	
INPUT DATA SKEW MARGIN	$T_{RSKM}$	500	_	_	ps	EACH DISPLAY	
						RESOLUTION	
						VID  = 200 mV	
CLOCK HIGH TIME	$T_{LVCH}$	_	$4/(7*R_{xFCLK})$	_	ns	$R_{xVCM} = 1.2V$	
						$R_{xFCLK} = 81MHz$	
CLOCK LOW TIME	$T_{LVCL}$		3/(7* R <sub>xFCLK</sub> )		ns		
PLL WAKE-UP TIME	$T_{enPLL}$			150	us	4	







MODEL NO.	VERSION	PAGE
ETML101025LDYA	5	8

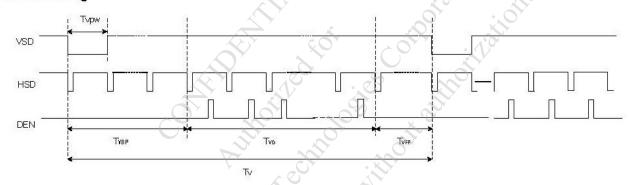
## 5.3 LVDS INTERFACE

## LVDS INPUT TIMING TABLE (DE MODE)

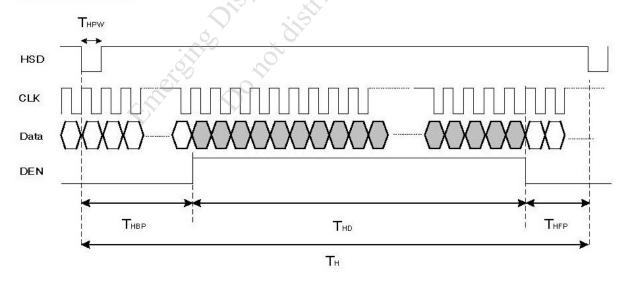
ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT
DCLK FREQUENCY @FRAME RATE=60Hz (LVDS)	Fdclk	68.2	72.4	78.5	MHz
HORIZONTAL DISPLAY AREA	Тнр		1280		DCLK
HSYNC PERIOD TIME	Тн	1380	1440	1500	DCLK
HSYNC BLANKING	T <sub>HBP</sub> +T <sub>HFP</sub>	100	160	220	DCLK
VERTICAL DISPLAY AREA	Tvd		800		Н
VSYNC PERIOD TIME	Tv	824	838	872	Н
VSYNC BLANKING	T <sub>VBP</sub> +T <sub>VFP</sub>	24	38	72	Н

NOTE: TIMING SETTING BASE ON 60Hz, FREQUENCY CAN BE ADJUSTED ACCORDING TO NEEDS, AS LONG AS IT DOES NOT AFFECT THE DISPLAY.

## Vertical timing

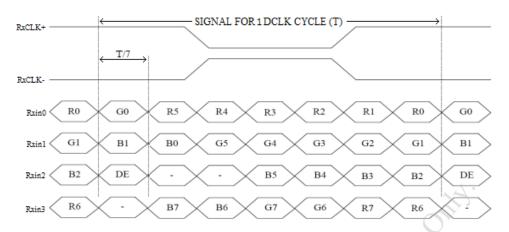


## Horizontal timing



MODEL NO. VERSION PAGE ETML101025LDYA 5 9

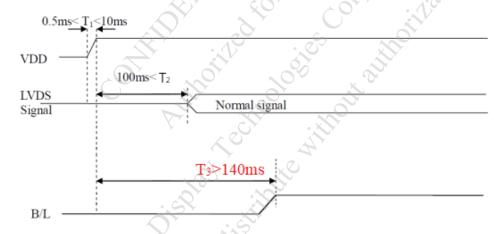
### 5.4 DATA INPUT FORMAT FOR LVDS



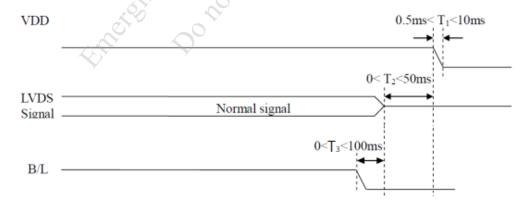
8BIT LVDS INPUT(VESA FORMAT)

## 5.5 POWER ON/OFF SEQUENCE

## 5.5.1 POWER ON SEQUENCE



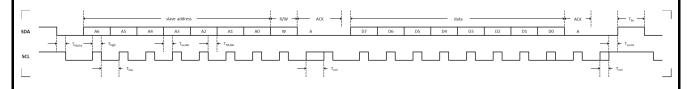
## 5.5.2 POWER OFF SEQUENCE



NOTE : VGH VOLTAGE SHOULD BE POWERED OFF EARLIER THAN VGL OR POWERED OFF AT THE SAME TIME.

MODEL NO.	VERSION	PAGE
ETML101025LDYA	5	10

### 5.6 CAPACITIVE TOUCH PANEL I2C INTERFACE TIMING CHARACTERISTICS



ITEL	CVAMPOI	MDI	3.4.37	IDIIT
ITEM	SYMBOL	MIN.	MAX.	UNIT
START BIT HOLD TIME	Thd;sta	600		ns
CLOCK HIGH PERIOD	Thigh	600		ns
CLOCK LOW PERIOD	Tlow	1300	-33	ns
DATA SETUP TIME	Tsu;dat	100		ns
DATA HOLD TIME	Thd;dat	0	<u> </u>	ns
MAXIMUM CLOCK STRETCH BY	Testr		5	ms
SLAVE STOP BIT SETUP TIME	Tsu;sto	600	-07	ns
BUS FREE TIME BETWEEN STOP		1200	XX	
AND START	Tbu	1300	.13	ns
AND START	plat recitions	Hilloux		

MODEL NO.	VERSION	PAGE
ETML101025LDYA	5	11

## 6. OPTICAL CHARACTERISTICS

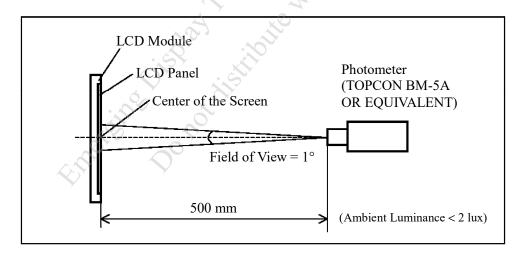
## 6.1 OPTICAL CHARACTERISTICS

Ta=25±2°C

i e		·			i e	l		1	1a 23±2 C				
ITEM		SYMBOL	COND	ITION	MIN.	TYP.	MAX.	UNIT	REMARK				
		$\theta$ y+		θx=0°	75	85		deg					
VIEWING ANGLE	MENUNG ANGLE		CD>10	0X-0	75	85	_	deg	NOTE (2)				
VIEWING ANGLE		$\theta x+$	CR≥10		75	85		deg	NOTE (3)				
		θx-		θy=0°		85		deg					
CONTRAST RATIO (CENTER)		CR	θx=0°, θy=0°		600	800		_	NOTE (3)				
RESPONSE TIME		T <sub>R</sub> (rise) +T <sub>F</sub> (fall)				25	50	msec	NOTE (4)				
	WHITE	Wx			0.26	0.31	0.36						
	WHILE	Wy			0.30	0.35	0.40						
COLOD	RED	Rx							0.55	0.60	0.65		
COLOR	KED	Ry	0 0°	0 0°	0.30	0.35	0.40		NOTE (5)				
CHROMATICITY (CENTER)	GREEN	Gx	$\theta x=0^{\circ}$ ,	•	0.26	0.31	0.36	· ·	NOTE (5)				
(CENTER)	GKEEN	Gy A	VDD-VS	SS=3.3 V BL-=12V	0.52	0.57	0.62	Y —					
	DITIE	Bx	LED B	M	0.10	0.15	0.20						
BLUE		By	PWM=		0.11	0.16	0.21						
THE BRIGHTNESS MODULE(CENTER)		В	1 WW -10070		1250	1350	_	cd/m <sup>2</sup>	NOTE (6)				
THE UNIFORMITY MODULE	OF	<u></u>	0	000	70			%	NOTE (7)				

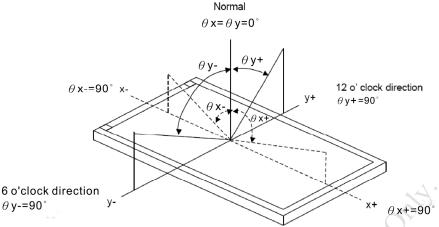
## NOTE (1): TEST CONDITION:

AFTER STABILIZING AND LEAVING THE PANEL ALONE AT A GIVEN TEMPERATURE FOR 30 MINUTES. MEASUREMENT SHOULD BE EXECUTED IN A STABLE, WINDLESS, AND DARK ROOM.



MODEL NO. VERSION PAGE ETML101025LDYA 5 12

NOTE (2): DEFINITION OF VIEWING ANGLE:

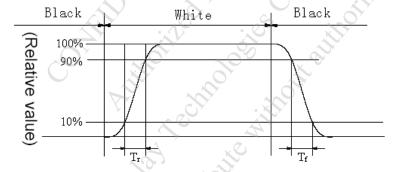


NOTE (3): DEFINITION OF CONTRAST RATIO (CR):

MEASURED AT THE CENTER POINT OF MODULE

 $\label{eq:contrast_ratio} \text{CONTRAST} \quad \text{RATIO(CR)} = \frac{\text{BRIGHTNESS MEASURED WHEN LCD IS AT "WHITE STATE"}}{\text{BRIGHTNESS MEASURED WHEN LCD IS AT "BLACK STATE"}}$ 

NOTE (4): DEFINITION OF RESPONSE TIME:  $T_R$  AND  $T_F$  THE FIGURE BELOW IS THE OUTPUT SIGNAL OF THE PHOTO DETECTOR.



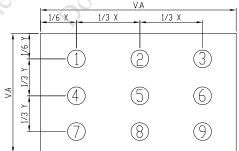
NOTE (5): DEFINITION OF COLOR CHROMATICITY

(a)100% RGB PIXEL DATA TRANSMISSION WHEN ALL THE INPUT TERMINALS OF MODULE ARE ELECTRICALLY POWERED ON.

(b)MEASURED AT THE CENTER POINT OF MODULE

NOTE (6): MEASURED THE BRIGHTNESS OF WHITE STATE AT CENTER POINT.

NOTE (7): (a) DEFINITION OF BRIGHTNESS UNIFORMITY



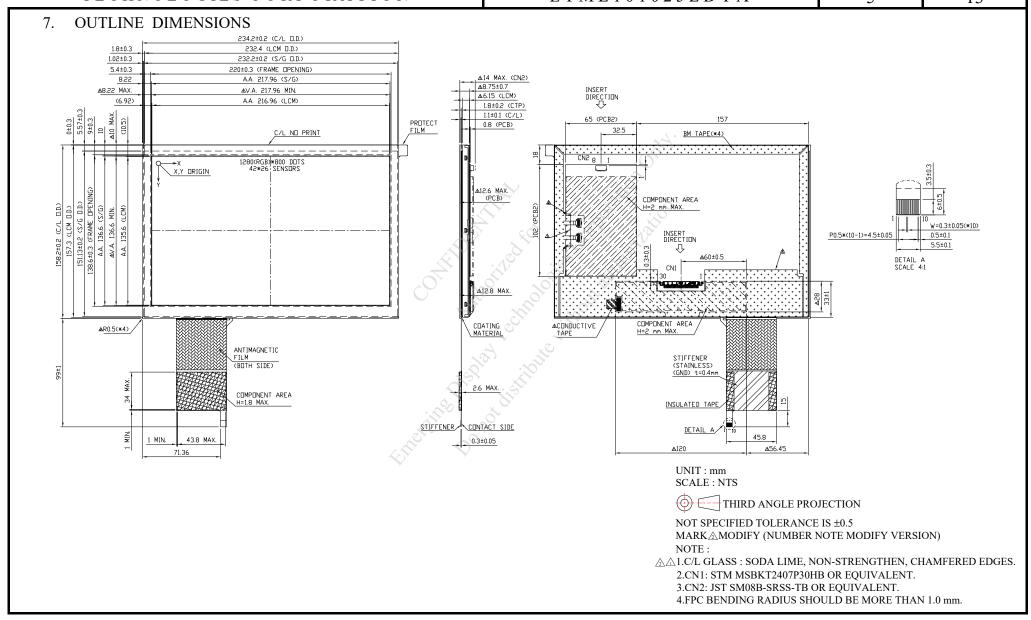
」UNIT:mm

(b)THE BRIGHTNESS UNIFORMITY CALCULATING METHOD

UNIFORMITY: MINIMUM BRIGHTNESS

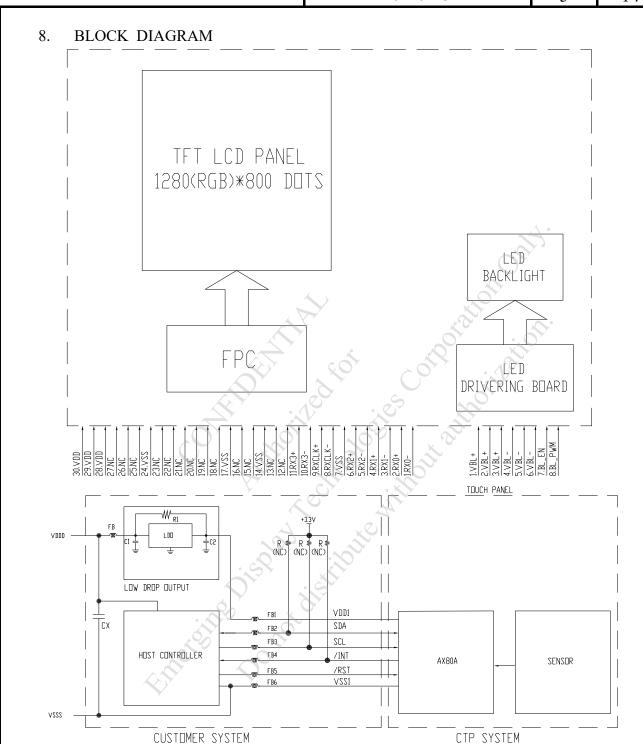
\*100%

MODEL NO.VERSIONPAGEETML101025LDYA513



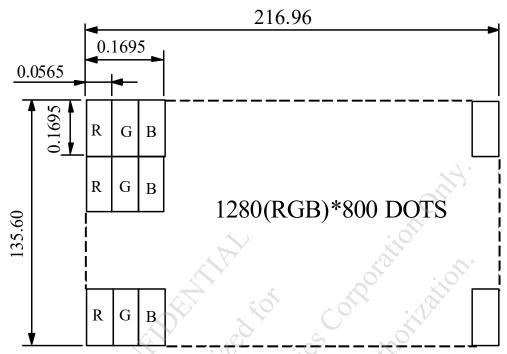
 MODEL NO.
 VERSION
 PAGE

 ETML101025LDYA
 5
 14



- NOTE (1): THE STANDARD IIC COMMUNICATION INTERFACE, SUPREME SCL CLOCK IS 400 KHz, SLAVE ADDRESS CAN BE SET UP, SUPPORTS VDD1 LEVEL POWER, NEEDS PULL HIGH RESISTANCE AND THE PULL HIGH RESISTORS ARE BUILT-IN FPCA.FOR CUSTOMER SYSTEM CAN BE RESEVRED THE PULL HIGH.
- NOTE ( 2 ) : POWER SUPPLY SHALL BE CLEAN AND NOISE FREE. ADDITIONAL FILTERING OR A SEPARATE LDO (LOW DROP OUT) REGULATOR CAN BE REQUIRED. C1 AND C2 CAPACITORS RECOMMENDATION : 4.7μF OR 10 μF





UNIT : mm SCALE : NTS

NOT SPECIFIED TOLERANCE IS ± 0.1
DOTS MATRIX TOLERANCE IS ± 0.01

MODEL NO. VERSION PAGE ETML101025LDYA 5 16

## 10. INTERFACE SIGNALS

## 10.1 TFT LCM (CN1)

PIN NO.	SYMBOL	FUNCTION
1	RX0-	NEGATIVE TRANSMISSION DATA OF PIXEL0
2	RX0+	POSITIVE TRANSMISSION DATA OF PIXEL0
3	RX1-	NEGATIVE TRANSMISSION DATA OF PIXEL1
4	RX1+	POSITIVE TRANSMISSION DATA OF PIXEL1
5	RX2-	NEGATIVE TRANSMISSION DATA OF PIXEL2
6	RX2+	POSITIVE TRANSMISSION DATA OF PIXEL2
7	VSS	GROUND
8	RXCLK-	NEGATIVE OF CLOCK
9	RXCLK+	POSITIVE OF CLOCK
10	RX3-	NEGATIVE TRANSMISSION DATA OF PIXEL3
11	RX3+	POSITIVE TRANSMISSION DATA OF PIXEL3
12	NC	NON CONNECTION
13	NC	NON CONNECTION
14	VSS	GROUND
15	NC	NON CONNECTION
16	NC	NON CONNECTION
17	VSS	GROUND
18	NC	NON CONNECTION
19	NC U	NON CONNECTION
20	NC	NON CONNECTION
21	NC	NON CONNECTION
22	NC	NON CONNECTION
23	NC	NON CONNECTION
24	VSS	GROUND
25	NC	NON CONNECTION
26	NC	NON CONNECTION
27	NC	NON CONNECTION
28	VDD	POWER SUPPLY VOLTAGE
29	VDD 💉	POWER SUPPLY VOLTAGE
30	VDD	POWER SUPPLY VOLTAGE

## 10.2 LED BACKLIGHT (CN2)

PIN NO.	SYMBOL	FUNCTION
1	VBL+	POWER SUPPLY VOLTAGE FOR LED DRIVER(+)
2	VBL+	POWER SUPPLY VOLTAGE FOR LED DRIVER(+)
3	VBL+	POWER SUPPLY VOLTAGE FOR LED DRIVER(+)
4	VBL-	POWER SUPPLY VOLTAGE FOR LED DRIVER(-)
5	VBL-	POWER SUPPLY VOLTAGE FOR LED DRIVER(-)
6	VBL-	POWER SUPPLY VOLTAGE FOR LED DRIVER(-)
7	BL_EN	BACKLIGHT LED ON/OFF CONTROL
8	BL_PWM	BACKLIGHT LED BRIGHTNESS CONTROL

MODEL NO.	VERSION	PAGE
ETML101025LDYA	5	17

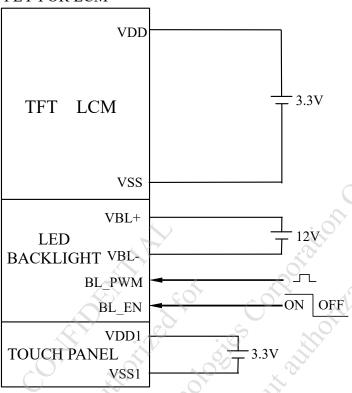
### 10.3 CTP

PIN NO.	SYMBOL	FUNCTION
1	VDD1	POWER SUPPLY VOLTAGE
2	/RST	EXTERNAL RESET, LOW IS ACTIVE
3	/INT	EXTERNAL INTERRUPT TO THE HOST
4	4.70	VACADA MANAGEMENT AND COMPANY
5	SCL	I2C CLOCK INPUT
6	VSS1	GROUND
7	VSS1	GROUND
8	NC	NON CONNECTION
9	NC	NON CONNECTION
10	NC	NON CONNECTION
		IZC CLOCK INPUT GROUND GROUND NON CONNECTION NON CONNECTION NON CONNECTION

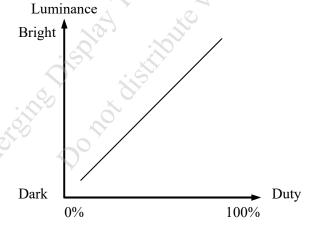
MODEL NO.	VERSION	PAGE
ETML101025LDYA	5	18

## 11. POWER SUPPLY

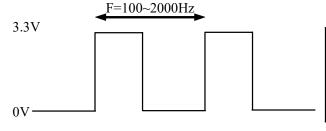
## 11.1 POWER SUPPLY FOR LCM



NOTE ( 1 ) : ADJUST THE PWM SIGNAL IN ORDER TO CONTROL LED BACKLIGHT'S BRIGHTNESS. THE HIGHER THE DUTY CYCLE, THE HIGHER THE BRIGHTNESS



NOTE (2): PWM SIGNAL OPERATION FREQUENCY IS 100~2000Hz AND DIMMING DUTY.



PWM Dimming	Dimming Duty		
Frequency[Hz]	Min[%]	Max[%]	
$100 < F_{DIM} < 200$	0.1	100	
$200 < F_{DIM} < 500$	0.4	100	
$200 < F_{DIM} < 1K$	0.8	100	
$1K < F_{DIM} < 2K$	1.5	100	

MODEL NO.	VERSION	PAGE
ETML101025LDYA	5	19

### 12. CAPACITIVE TOUCH PANEL SPECIFICATION

### 12.1 OPTICAL CHARACTERISTICS

ITEM	CONDITION	MIN.	TYP.	MAX.	UNIT
TRANSPARENCY NOTE ( 1 )	$Ta = 25^{\circ}C$ $\lambda = 550$ nm	85	_	_	%

NOTE (1): OPTICAL MEASUREMENT SHOULD BE EXECUTED AFTER PANEL IS SECURED.

MEASUREMENT PROCESS SHOULD BE EXECUTED IN A STABLE, WINDLESS, AND DARK ROOM. OPTICAL SPECIFICATIONS SHOULD BE MEASURED BY SPECTROPHOTOMETER.

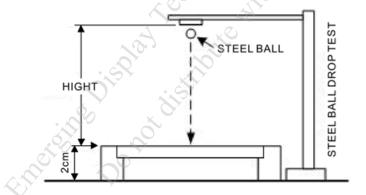
## 12.2 HARDNESS

ITEM	DESCRIPTION
SURFACE HARDNESS	7H (MIN.)

## 12.3 DURABILITY

USING STEEL BALL AND FALLING ON TOUCH PANEL SURFACE, FROM THE HEIGHT MUST PASS BELOW CONDITIONS:

ITEM	CONDITION	INSPECTION METHOD	DESCRIPTION
STEEL BALL DROP TEST	WEIGHT: 67g HEIGHT OF FALL: 30 cm	VISUAL	SIGN OF FRACTURE OR DAMAGE IS NOT ACCEPTABLE 3 TIMES/ 1 POINTS, 25°C(CENTER POINT)



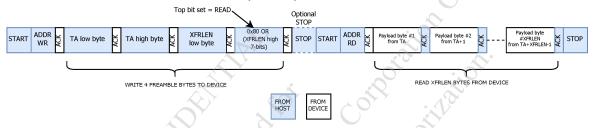
MODEL NO.	VERSION P.	
ETML101025LDYA	5	20

### 12.4 PROTOCOL

#### 12.4.1 I2C BUS PROTOCOL

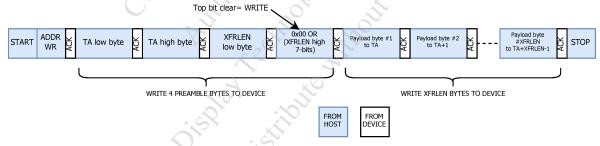
IN THE FOLLOWING DIAGRAMS ADDR IS THE 7-BITI2C BUS ADDRESS, START/STOP ARE THE NORMAL BUS START AND STOP CONDITIONS RESPECTIVELY, WR DENOTES THAT THE 8TH BIT OF THE ADDRESS BYTE IS CLEAR TO REQUEST A WRITE TRANSFER WHEREAS RD DENOTES THAT IT IS SET TO REQUEST A READ TRANSFER. THE DEVICE WILL APPLY A CLOCK STRETCH WHEN IT NEEDS TO DECODE OR PREPARE DATA BETWEEN THE VARIOUS PHASES.

### DATA FROM DEVICE TO HOST (READ)



### **I2C READ TRANSFER**

### DATA FROM HOST TO DEVICE (WRITE)



**I2C WRITE TRANSFER** 

SLAVE I2C ADDRESS=0x66

MODEL NO. VERSION PAGE ETML101025LDYA 5 21

### 13. INSPECTION CRITERIA

### 13.1 APPLICATION

THIS INSPECTION STANDARD IS TO BE APPLIED TO THE LCD MODULE DELIVERED FROM EMERGING DISPLAY TECHNOLOGIES CORP.( E.D.T ) TO CUSTOMERS

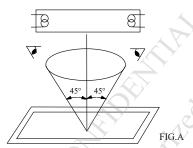
### 13.2 INSPECTION CONDITIONS

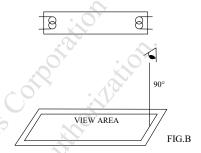
### 14.2.1 (1)OBSERVATION DISTANCE: 45±5cm

(2) VIEWING ANGLE: ±45°

±45° (FOR SECTION WITHIN VIEWING AREA), REFER TO FIG.A 90° (FOR SECTION OUTSIDE OF VIEWING AREA), REF TO FIG.B PERPENDICULAR TO MODULE SURFACE

VIEWING ANGLE SHOULD BE SMALLER THAN 45°





THE INSPECTION CRITERIA IS ACCORDING TO LINE OF SIGHT. INSPECTION SHALL BE MADE WITHIN THE HALF SECTION OF THE VIEWING CONE GENERATED BY LINE SEGMENT OF 45° WITH RESPECT TO THE VERTICAL AXIS FROM CENTER VERTEX OF LCD, THE FLUORESCENT LAMP AND THE CONE AXIS MUST BE PERPENDICULAR TO THE LCD SURFACE.

IF THE DEFECTS ARE OUTSIDE OF VIEWING AREA, IT SHALL BE INSPECTED BY 90° WITH RESPECT TO THE VERTICAL AXIS FROM EDGE OF VIEWING AREA.

### 13.2.2 ENVIRONMENT CONDITIONS:

AMBIEN	25±5°C	
AMB	$65 \pm 20\%$ RH	
AMBIENT	COSMETIC INSPECTION	600~800 lux
ILLUMINATION	FUNCTIONAL INSPECTION	300~500 lux
INS	15 secs	

## 13.2.3 INSPECTION LOT QUANTITY PER DELIVERY LOT FOR EACH MODEL

### 13.2.4 INSPECTION METHOD

A SAMPLING INSPECTION SHALL BE MADE ACCORDING TO THE FOLLOWING PROVISIONS TO JUDGE THE ACCEPTABILITY (a)APPLICABLE STANDARD:

ANSI/ ASQ Z1.4 NORMAL INSPECTION LEVEL II

(b)AQL: MAJOR DEFECT: AQL 0.65 MINOR DEFECT: AQL 1.5

MODEL NO. VERSION PAGE ETML101025LDYA 5 22

## 13.3 DEFECTS CLASSIFICATION

INSPECTION ITEM	DEFECT FEATURE	AQL	
1.DISPLAY ON	DEFECT TO MISS SPECIFIED     DISPLAY FUNCTION, FOR ALL     AND SPECIFIED DOTS     EX: DISCONNECTION, SHORT     CIRCUIT ETC	0.65	
2.CTP FUNCTION	<ul><li>NO FUNCTION</li><li>BROKEN LINE</li><li>FALSE TOUCH</li></ul>		
3.BACKLIGHT	<ul><li>NO LIGHT</li><li>FLICKERING AND OTHER ABNORMAL ILLUMINATION</li></ul>		
4.DIMENSIONS	SUBJECT TO INDIVIDUAL     ACCEPTANCE SPECIFICATIONS		
1.DISPLAY ZONE (VIEWING AREA)	<ul> <li>BLACK/WHITE SPOT / CIRCULAR TYPE</li> <li>BUBBLES ON POLARIZER</li> <li>NEWTON RING</li> <li>BLACK/WHITE LINE / LINEAR TYPE</li> <li>SCRATCH</li> <li>CONTAMINATION</li> <li>UNEVEN COLOR SPREAD</li> </ul>		
2.BEZEL ZONE	• STAINS • SCRATCHES • FOREIGN MATTER	1.5	
3.SOLDERING 4.DISPLAY ON	<ul> <li>INSUFFICIENT SOLDER</li> <li>SOLDERED IN INCORRECT POSITION</li> <li>CONVEX SOLDERING SPOT</li> <li>SOLDER BALLS</li> <li>SOLDER SCRAPS</li> <li>LIGHT LINE</li> </ul>		
	1.DISPLAY ON  2.CTP FUNCTION  3.BACKLIGHT  4.DIMENSIONS  1.DISPLAY ZONE (VIEWING AREA)  2.BEZEL ZONE  3.SOLDERING	DEFECT TO MISS SPECIFIED DISPLAY FUNCTION, FOR ALL AND SPECIFIED DOTS EX: DISCONNECTION, SHORT CIRCUIT ETC      NO FUNCTION     BROKEN LINE     FALSE TOUCH      NO LIGHT     FLICKERING AND OTHER ABNORMAL ILLUMINATION      SUBJECT TO INDIVIDUAL ACCEPTANCE SPECIFICATIONS      BLACK/WHITE SPOT / CIRCULAR TYPE     BUBBLES ON POLARIZER     NEWTON RING     BLACK/WHITE LINE / LINEAR TYPE     SCRATCH     CONTAMINATION     UNEVEN COLOR SPREAD      STAINS     SCRATCHES     FOREIGN MATTER      INSUFFICIENT SOLDER     SOLDER DIN INCORRECT POSITION     CONVEX SOLDERING SPOT     SOLDER BALLS     SOLDER SCRAPS  4.DISPLAY ON      LIGHT LINE	

MODEL NO. VERSION PAGE ETML101025LDYA 5 23

NO.	ITEM	CRITERIA		
1	INSPECTION	1. INCORRECT PATTERN 2. MISSING SEGMENT 3. DIM SEGMENT 4. OPERATING VOLTAGE BEYOND SPEC		
2	OVERALL DIMENSIONS	1. OVERALL DIMENSION BEYOND SPEC		
3	DOT DEFECT	1. INSPECTION PATTERN: FULL WHITE, FULL BLACK, RED, GREEN AND BLUE SCREENS.  2.    ITEMS		
4	BUBBLES ON POLARIZER /SURFACE STAINS /DIRT/CF FAIL/SPOT	AVERAGE DIAMETER (mm): D PERMITTED  BUBBLE ON THE POLARIZER  D \( \) 0.3 \( \) D \( \) 0.5 \( \) N \( \) S  USURFACE STAINS  D \( \) 0.1 \( \) D \( \) 0.5 \( \) N \( \) NONE  SURFACE STAINS  0.1 \( \) D \( \) 0.5 \( \) NONE  W \( \) 0.05 \( \) NONE  SCRATCHES  D \( \) 0.05 \( \) N \( \) S  O \( \) O \( \) NONE  NOTE: 1. POLARIZER BUBBLE IS DEFINED AS THE BUBBLE APPEARS OF ACTIVE DISPLAY AREA. THE DEFECT OF POLARIZER BUBBLE SHALL BE IGNORED IF THE POLARIZER BUBBLE APPEARS OF THE OUTSIDE OF ACTIVE DISPLAY AREA.  2. THE EXTRANEOUS SUBSTANCE IS DEFINED AS IT CAN BE OBSERVED WHEN THE MODULE IS POWER ON. THE DEFINITION OF AVERAGE DIAMETER, D IS DEFINED AS FOLLOWING.  AVERAGE DIAMETER (D)=(a+b)/2		

 MODEL NO.
 VERSION
 PAGE

 ETML101025LDYA
 5
 24

	<u> </u>		CRITERIA	
	BLACK/WHITE SPOT CIRCULAR TYPE	THE FOLLOWING BLACK/WHITE SE		
		VIEWING AREA. AVERAGE DIAMET	. ,	/ D
5		SIZE D	PERMISSIBLE NO.	D / D
		D≤0.3	IGNORE	
5		0.3 <d≤0.5< td=""><td>6</td><td></td></d≤0.5<>	6	
		D>0.5	0	
		NOTE (1): THE DISTANCE BETW		
		SHOULD BE MORE TH		
		THE FOLLOWING SCRATCH IS WITH		
		WIDTH: W (mm), LENGTH: L (mm)		I I
		SIZE W & L	PERMISSIBLE NO.	
6	SCRATCH	W≤0.07	IGNORE	
		0.07 <w≤0.1, l≤10<="" td=""><td>6</td><td></td></w≤0.1,>	6	
		W>0.1	0	
		NOTE (1): THE DISTANCE BETW		
		SHOULD BE MORE TH		
		THE FOLLOWING BLACK LINE, WH		
		VIEWING AREA. WIDTH: W (mm),		
	BLACK /	SIZE W & L	PERMISSIBLE NO.	
7	WHITE LINE LINEAR TYPE /	W≤0.07	IGNORE	
	FOREIGN FIBER	0.07 <w≤0.1, l≤10<="" td=""><td>6</td><td></td></w≤0.1,>	6	
	POREIGN FIBER	W>0.1 NOTE (1): THE DISTANCE BETW		
		SHOULD BE MORE TH BUBBLES WITHIN VIEWING AREA.	AN 10mm APAR1.	
		AVERAGE DIAMETER: D (mm)	20	
		SIZE D	PERMISSIBLE NO.	/
	BUBBLE / DENT	D≤0.3	IGNORE	D _
8	FOR OPTICAL	0.3 <d≤0.5< td=""><td>6</td><td></td></d≤0.5<>	6	
	BONDING	D>0.5	0	<b>7</b>
		NOTE (1): THE DISTANCE BETW		
		SHOULD BE MORE TH		
			$mm, Y \le 3mm \cdot Z \le t$	Chip of glass
		I I CORNER I	(t: THICKNESS)	v
9	CHIPPING	V<	$6mm, Y \le 1mm, Z < t$	
			(t : THICKNESS)	
10	CRACKED GLASS	NOT ACCEPTABLE		,
11	LINE DEFECT ON	OBVIOUS VERTICAL OR HORIZONTAL I	LINE DEFECT IS NOT ALLOWE	D
	DISPLAY MURA ON		<b>5</b> 0/ N.D. DY <b>S</b> DD	
12	DISPLAY	MURA NOT VISIBLE THROUGH	5% ND FILTER	
	UNEVEN COLOR	0		
13	SPREAD,	1. TO BE DETERMINED BASED UPON TH	E STANDARD SAMPLE.	
	COLORATION	1. BEZEL MAY NOT HAVE RUST, BE DEF	ORMED OR HAVE EINGER	
14	BEZEL	PRINTS STAINS OF OTHER CONTAMIN		
	APPEARANCE	2. BEZEL MUST COMPLY WITH JOB SPEC		
		1. THERE MAY NOT BE MORE THAN 2mr	n OF SEALANT OUTSIDE THE S	SEAL AREA ON THE I
		AND THERE SHOULD BE NO MORE THE 2. NO OXIDATION OR CONTAMINATION		
		2. NO OXIDATION OR CONTAMINATION 3. PARTS ON PCB MUST BE THE SAME A		ACTERISTIC CHART
15	PCB	THERE SHOULD BE NO WRONG PART		
		4. THE JUMPER ON THE PCB SHOULD CO		
	i .	5. IF SOLDER GETS ON BEZEL TAB PADS	TED DAD ZEDDA DAD OD CD	EWILD DAD CMAI

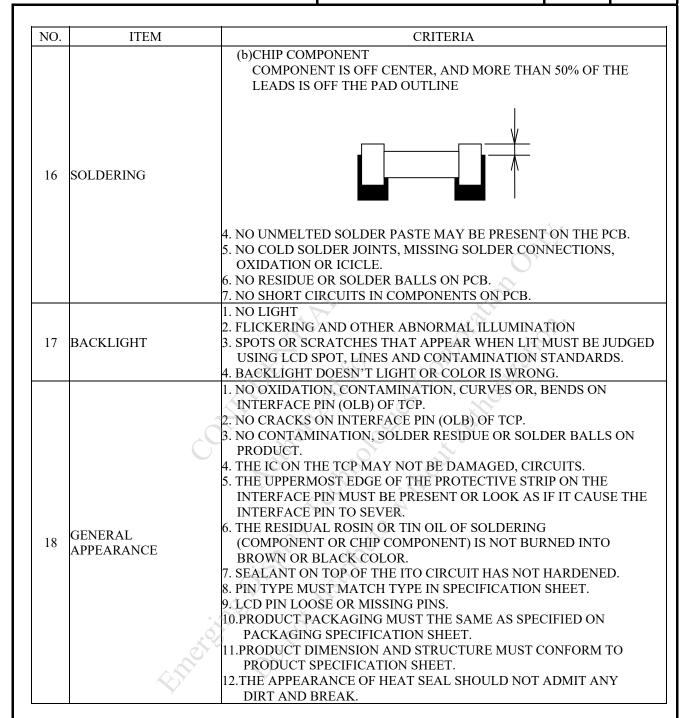
## EMERGING DISPLAY

TECHNOLOGIES CORPORATION ETML101025LD

MODEL NO. VERSION PAGE ETML101025LDYA 5 25

MODEL NO.
ETML101025LDYA
5

VERSION PAGE 5 26



MODEL NO.	VERSION	PAGE
ETML101025LDYA	5	27

### 14. RELIABILITY TEST

### 14.1 STANDARD SPECIFICATIONS FOR RELIABILITY OF LCD MODULE

NO.	ITEM	DESCRIPTION
1	HIGH TEMPERATURE TEST(OPERATION)	THE SAMPLE SHOULD BE ALLOWED TO STAND AT +85°C FOR 240 HRS
2	LOW TEMPERATURE TEST(OPERATION)	THE SAMPLE SHOULD BE ALLOWED TO STAND AT -30°C FOR 240 HRS
3	HIGH TEMPERATURE TEST (STORAGE)	THE SAMPLE SHOULD BE ALLOWED TO STAND AT +85°C FOR 240 HRS
4	LOW TEMPERATURE TEST (STORAGE)	THE SAMPLE SHOULD BE ALLOWED TO STAND AT -40°C FOR 240 HRS
5	HIGH TEMPERATURE / HUMIDITY TEST (STORAGE)	THE SAMPLE SHOULD BE ALLOWED TO STAND AT 60°C, 90% RH 240 HRS
6	THERMAL SHOCK (NOT OPERATED)	THE SAMPLE SHOULD BE ALLOWED TO STAND THE FOLLOWING 10 CYCLES OF OPERATION:  +85°C  -40°C  30 min 5 min 30 min 5 min 1 5 min 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1
7	ESD (ELECTROSTATIC DISCHARGE) (NOT OPERATED)	AIR DISCHARGE ± 12KV CONTACT DISCHARGE ± 8KV (ACCORDING TO IEC-61000-4-2)

- NOTE (1): THE TEST SAMPLES HAVE RECOVERY TIME FOR 2 HOURS AT ROOM TEMPERATURE BEFORE THE FUNCTION CHECK. IN THE STANDARD CONDITIONS, THERE IS NO DISPLAY FUNCTION NG ISSUE OCCURRED.
- NOTE (2): THERE IS NO DISPLAY FUNCTION NG ISSUE OCCURRED, ALL THE COSMETIC SPECIFICATION IS JUDGED BEFORE THE RELIABILITY STRESS.
- NOTE (3): THE MODULE SHOULDN'T BE TESTED MORE THAN ONE CONDITION, AND ALL THE TEST CONDITIONS ARE INDEPENDENT.
- NOTE (4): WHEN THE LCD MODULE IS OPERATED AT A HIGHER AMBIENT TEMPERATURE THAN 60°C, THE PWM DUTY CYCLE OF THE LED BACKLIGHT SHOULD BE ADJUSTED TO BE LESS THAN (TBD)%. IF THE MODULE IS OPERATED AT A HIGHER DUTY CYCLE THAN (TBD)%, THEN THERE IS A POSSIBILITY OF DISTORTION AND IRREGULARITY OF THE PICTURE DUE TO LIQUID CRYSTAL BEHAVIOR.
- NOTE (5): TESTING CONDITIONS AND INSPECTION CRITERIA

NO.	ITEM	TEST MODEL	INSPECTION CRITERIA
1	CURRENT CONSUMPTION		THE CURRENT CONSUMPTION SHOULD CONFORM TO THE PRODUCT SPECIFICATION.
2	CONTRAST	REFER TO SPECIFICATION	AFTER THE TESTS HAVE BEEN EXECUTED, THE CONTRAST MUST BE LARGER THAN HALF OF ITS INITIAL VALUE PRIOR TO THE TESTS.
3	APPEARANCE	VISUAL INSPECTION	DEFECT FREE

MODEL NO.	VERSION	PAGE
ETML101025LDYA	5	28

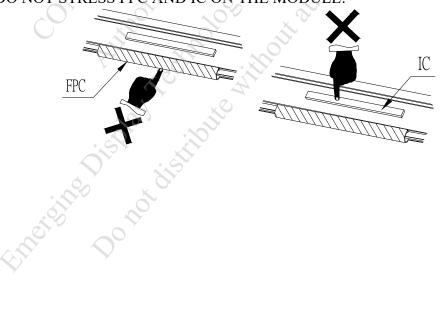
### 15. CAUTION

## 15.1 OPERATION

- 15.1.1 DO NOT CONNECT OR DISCONNECT MODULES TO OR FROM THE MAIN SYSTEM WHILE POWER IS BEING SUPPLIED .
- 15.1.2 USE THE MODULE WITHIN SPECIFIED TEMPERATURE; LOWER TEMPERATURE CAUSES THE RETARDATION OF BLINKING SPEED OF THE DISPLAY; HIGHER TEMPERATURE MAKES OVERALL DISPLAY DISCOLOR. WHEN THE TEMPERATURE RETURNS TO NORMALITY, THE DISPLAY WILL OPERATE NORMALLY.
- 15.1.3 ADJUST THE LC DRIVING VOLTAGE TO OBTAIN THE OPTIMUM CONTRAST.
- 15.1.4 POWER ON SEQUENCE INPUT SIGNALS SHOULD NOT BE SUPPLIED TO LCD MODULE BEFORE POWER SUPPLY VOLTAGE IS APPLIED AND REACHES THE SPECIFIED VALUE.

  IF ABOVE SEQUENCE IS NOT FOLLOWED, CMOS LSIS OF LCD MODULES MAY BE DAMAGED DUE TO LATCH UP PROBLEM.
- 15.1.5 NOT ALLOWED TO INFLICT ANY EXTERNAL STRESS AND TO CAUSE ANY MECHANICAL INTERFERENCE ON THE BENDING AREA OF FPC DURING THE TAIL BENDING BACKWARDS!

  DO NOT STRESS FPC AND IC ON THE MODULE!



MODEL NO. VERSION PAGE ETML101025LDYA 5 29

### 15.2 NOTICE

- 15.2.1 USE A GROUNDED SOLDERING IRON WHEN SOLDERING CONNECTOR I/O TERMINALS . FOR SOLDERING OR REPAIRING, TAKE PRECAUTION AGAINST THE TEMPERATURE OF THE SOLDERING IRON AND THE SOLDERING TIME TO PREVENT PEELING OFF THETHROUGH-HOLE-PAD .
- 15.2.2 DO NOT DISASSEMBLE . EDT SHALL NOT BE HELD RESPONSIBLE IF THE MODULE IS DISASSEMBLED AND UPON THE REASSEMBLY THE MODULE FAILED .
- 15.2.3 DO NOT CHARGE STATIC ELECTRICITY, AS THE CIRCUIT OF THIS MODULE CONTAINS CMOS LSIS. A WORKMAN'S BODY SHOULD ALWAYS BE STATIC-PROTECTED BY USE OF AN ESD STRAP. WORKING CLOTHES FOR SUCH PERSONNEL SHOULD BE OF STATIC-PROTECTED MATERIAL.
- 15.2.4 ALWAYS GROUND THE ELECTRICALLY-POWERED DRIVER BEFORE USING IT TO INSTALL THE LCD MODULE. WHILE CLEANING THE WORK STATION BY VACUUM CLEANER, DO NOT BRING THE SUCKING MOUTH NEAR THE MODULE; STATIC ELECTRICITY OF THE ELECTRICALLY-POWERED DRIVER OR THE VACUUM CLEANER MAY DESTROY THE MODULE.
- 15.2.5 DON'T GIVE EXTERNAL SHOCK.
- 15.2.6 DON'T APPLY EXCESSIVE FORCE ON THE SURFACE.
- 15.2.7 LIQUID IN LCD IS HAZARDOUS SUBSTANCE. MUST NOT LICK AND SWALLOW.

  WHEN THE LIQUID IS ATTACH TO YOUR, SKIN, CLOTH ETC.

  WASH IT OUT THOROUGHLY AND IMMEDIATELY.
- 15.2.8 DON'T OPERATE IT ABOVE THE ABSOLUTE MAXIMUM RATING.
- 15.2.9 STORAGE IN A CLEAN ENVIRONMENT, FREE FROM DUST, ACTIVE GAS AND SOLVENT.
- 15.2.10 STORE WITHOUT ANY PHYSICAL LOAD.
- 15.2.11 REWIRING: NO MORE THAN 3 TIMES.